Analyzing the Performance of the Epiphany Processor

Trygve Aaberge

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Supervisor: Anne Cathrine Elster, IDI

Norwegian University of Science and Technology
Department of Computer and Information Science
Assignment

The Parallella computer by Adapteva is a new, small and energy efficient computer, that equips the 16 core co-processor Epiphany, also designed by Adapteva.

This project will focus on analyzing the performance of the Epiphany co-processor. Several test cases will be developed. Tests on how well they perform on the Epiphany in comparison to other processors, will be included.
Abstract

Our need for computational power steadily increases as we strive to solve more complex problems. As the increase of computational power of central processing units (CPUs) is slowing down, due to increasingly smaller and more complex designs, it may be a good idea to look into different processor designs.

At the same time, the need for low-power processors are increasing. There is many reasons for this. The desire to reduce costs and produce more environmentally friendly data centers is one. Another is to reduce the heat dissipation, which is more important the smaller the processor design is. Lastly, mobile devices are becoming increasingly popular.

In this project, I will look into the performance of the Epiphany processor using the Parallella device. This is a new design of a small and low-power processor. The processor I am testing contains 16 processor cores. There is also produced a version with with 64 cores, and future versions will have even more.

I will implement algorithms for suited problems and measure the run times of these on the Epiphany. To have a reference point for the performance of the Epiphany, similar implementations are run and measured on the main processor of the Parallella, a Xilinx Zynq7020.
Sammendrag

Behovet for prosessorkraft er stadig økende mens vi streber etter å løse problemer av større kompleksitet. Nå som økningen i prosessorkraft begynner å avta, på grunn av mindre og mer komplekse prosessordesign, kan det være en god idé å undersøke forskjellige prosessordesign.

På samme tid øker behovet for energieffektive prosessorer. Det er mange grunner til dette. Ønsket om å redusere kostnader og produsere mer miljøvennlige datasentere er en grunn. En annen er å redusere varmetapet, som er viktigere jo mindre prosessoren er. Til sist blir mobile enheter stadig mer populært.


Jeg kommer til å implementere algoritmer for egne problemer og måle kjøretiden av disse på Epiphany-prosessoren. For å ha et referansepunkt for ytelsen til Epiphany-prosessoren, vil liknende implementeringer kjøres og måles på hovedprosessoren til Parallella-enheten, en Xilinx Zynq7020.
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Abbreviations

COPRTHR  CO-PRocessing THReads
MIMD  multiple instruction, multiple data
OpenCL  Open Computing Language
SDK  software development kit
SIMD  single instruction, multiple data
eSDK  Epiphany Software Development Kit
Chapter 1

Introduction

1.1 Motivation

There are always interest in creating new methods of computing, and improving on existing ones. However, it may not always be possible to improve the design of a specific technology further.

This is becoming the case of traditional processor design now. Because of heat dissipation, it is not possible to increase the clock frequency of the processors used today.

There is also a change in usage. Mobile devices are becoming increasingly used, and requires more and more performance. However, improvements in the battery technology does not increase as fast as the performance.

For these reasons, we need to think in new ways.

1.2 The Parallella Device

The Parallella is a very small computer. It is capable of performing all the tasks of any kind of computer, but it is especially suited for some tasks. The size of the device is about the size of a credit card in width and length, and about one centimeter high. It features a Dual-Core ARM central processing unit (CPU) and 1 GB of RAM, but its main trait is the co-processor it has. This co-processor is a special processor called Epiphany, which has many cores over a very small amount of physical space. It is created by Adapteva, the same company that created the Parallella. The Parallella is the first consumer device to equip this processor.

The Epiphany is a processor that is created for parallel tasks. In contrast to standard processors that we find in todays computers, the Epiphany has many more cores. In this first version of the processor, it has 16 cores.
They have recently finished another version, with 64 cores\cite{10}. In the future, they will create future versions with even more cores, or combine multiple 64-core Epiphany processors in an array. This way, the platform should be suited for massively parallel tasks. Adapteva claims that the Epiphany architecture should support up to 4096 cores, all sharing a single memory space.

While the Epiphany is very parallel, it has some drawbacks compared to a traditional processor, which is the main reason that the Parallella uses it as its co-processor and has another processor as its main processor. The Epiphany uses a unique instruction set created specifically for this processor, which means that programs will have to be compiled specifically for this processor. However, it may still be programmed with ANSI C and it supports pthreads and Open Computing Language (OpenCL) as well.

The memory architecture of the Epiphany is unusual as it has no local caches or memory hierarchy. Instead, each core has a small amount of memory, and it is up to the programmer how it should be used. This is one of the ways the processor is able to employ so many cores on such a small space.

The cores of the processor uses a multiple instruction, multiple data (MIMD) architecture. It is therefore possible to assign each core with its own task, which does not have to be related to the tasks of the other cores. This is in contrast to a graphics processing unit (GPU), which uses a single instruction, multiple data (SIMD) architecture, where each core must work
on the same task simultaneously with different sets of data.

One of the other remarkable features of the Epiphany, apart from the many cores relative to its size, is the power consumption of the processor. Both the version of the processor with 16 cores and the one with 64 cores has a maximum power consumption of 2 Watts.

1.3 Problems suited for the Epiphany

There are some factors that make some problems well suited for the Epiphany, and other problems less suited. Of course, the tasks should be parallel, as the main feature of the Epiphany is its parallel nature. However, since the architecture is MIMD, it does not have to be a task that is only parallel in its data, though it very well may be. Another factor is the memory of the Epiphany. Since the processor has a so little amount of memory it should not be a task that requires very high amounts of data. Last, the data transfer between the main processor of the Parallella and the Epiphany is not very fast. This means that the Epiphany is best suited for generating data, or processing a small amount of data many times. We would want to transfer a small amount of data to the Epiphany, then work on this data in many iterations, and at last transfer the result back to the main processor.

One of the simplest parallel tasks are vector and matrix multiplication. For each pair of fields of the source vectors or matrices, there is done a calculation. Each of these calculations can be spread over equally many processors.

An interesting problem is the calculation of the Mandelbrot set. This is a complex calculation which takes some initial values and does iterations of a particular mathematical operation. Hence, we can do many calculations on the Epiphany, without transferring much data to it. The calculations operates independently on a grid of values and is therefore very parallelizable.

I also want to look at Conway’s Game of Life[11]. This consists of a grid of values that are considered to be either dead or alive. It takes an initial input, and calculates a new set of values from this. The algorithm can run for an arbitrary number of frames, and each new frame is calculated from the past. Each value is calculated only by using its neighbours values, and it is therefore very parallelizable.
1.4 Methods of using the Epiphany

The Epiphany supports ANSI C with support for pthreads and OpenCL. There are two SDKs you may use to program for the Epiphany: The Epiphany Software Development Kit (eSDK) or the CO-PRocessing THReads (COPRTHR) software development kit (SDK).

The eSDK[6] is an SDK which enables you to compile C code for the Epiphany. The implementation provides support for assigning different tasks to different cores. It also implements pthreads.

The COPRTHR[2] SDK is another SDK which provides an OpenCL implementation. By using this, you may easily port OpenCL applications to the Epiphany.

1.5 Outline of Chapters

The rest of this report will be organized as follows:

- Chapter 2: Different programs for measuring performance and the results of running these on the Epiphany compared to the main processor of the Parallella.
- Chapter 3: Discussion of the results gathered in the previous chapter.
- Chapter 4: Short summary of the work done in this project and the results of this. Also includes thoughts of what related future projects may look into.
- Appendix: Includes the code written and used in this project.
Chapter 2

Results

In this chapter I will present what kind of implementations I made, and the results I got from running these. For comparison of run times relative to the Epiphany, I have also run similar implementations on the main processor of the Parallella, a Xilinx Zynq7020[8]. Since the implementations for the Epiphany uses special libraries to utilize its cores, I could not directly run the same code on the main processor. Therefore I implemented single threaded programs in standard C using the same algorithms.

This Chapter is organized as follows:

- Section 2.1: Results gathered using the COPRTHR SDK. This contains implementations of vector multiplication and computation of the Mandelbrot set.
- Section 2.2: Results gathered using the eSDK. This contains an implementation of Conway’s Game of Life.

2.1 COPRTHR

Since I had some previous experience with OpenCL, the COPRTHR SDK seemed like the easiest of the two libraries to use.

One very simple parallel task is to multiply two vectors. We transfer each of the vectors onto the memory of the co-processor. Then, each processor handles its own part of the vector and multiplies each element in its part of one of the vectors with the corresponding element in the other vector. The result is stored in a third vector.

Unfortunately, the performance of each of the Epiphany cores seems to be 50 to 100 times slower than the core of the main processor of the Parallella.
If we combine all of the cores, it then gives 3 to 6 times worse performance than one of the cores of the main processor.

Figure 2.1: A visualization of the Mandelbrot set[1].

Another problem that may be suited for the Epiphany is calculating the Mandelbrot set. With this problem, we only have to transfer some initial parameters. We may then do many calculations based on these, and at last transfer the result back. For each of the values transferred back, we would have done many iterations on the Epiphany. Unfortunately, so far, the results I have found is very poor. The calculation on the Epiphany seems to be over a 1000 times slower than the calculation on the main processor.

2.2 eSDK

Because of the poor results I experienced with the COPRTHR SDK, I decided to test the eSDK. Initially, I had some issues with running code using this SDK. However, I was directed at an example of a implementation of computing the Mandelbrot set using it[7]. This code showed how to compile and run the code.

Since computation of the Mandelbrot set already was implemented, I decided that I would implement Conway’s Game of Life. I made an implementation for the Epiphany processor, using the Mandelbrot example as help on how to implement it. For benchmarking, an implementation for another processor was also needed. I decided to implement a single threaded version using standard C. For comparison with the Epiphany, I ran this on the main processor of the Parallella.
In the single threaded implementation in C, I could use arrays to store values of each of the fields of the board. Since the main goal is to compare performance with the Epiphany, I also created an implementation that allocated a memory space and used that manually. This is in a similar fashion to how the implementation for the Epiphany is.

For the benchmarking, I used 180 rows, 180 columns and the same starting pattern for each run. The results are presented here. The first columns is the number of frames, or steps, the programs was running for. The rest of the columns is the results for respectively the parallel Epiphany implementation, the serial implementation on the main processor using arrays and the serial implementation on the main processor using an allocated memory space. The results are running time in seconds.

<table>
<thead>
<tr>
<th>Frames</th>
<th>Epiphany</th>
<th>Xilinx, array</th>
<th>Xilinx, malloc</th>
</tr>
</thead>
<tbody>
<tr>
<td>100</td>
<td>0.223</td>
<td>3.222</td>
<td>3.917</td>
</tr>
<tr>
<td>2000</td>
<td>4.429</td>
<td>59.506</td>
<td>79.031</td>
</tr>
<tr>
<td>10000</td>
<td>22.106</td>
<td>291.853</td>
<td>355.537</td>
</tr>
</tbody>
</table>
Chapter 3

Discussion

In this chapter, I will discuss the results presented in the previous chapter. This Chapter is organized as follows:

- Section 3.1: Discussion of the results gathered using the COPRTHR SDK. Theories of why the performance using this SDK was poor.
- Section 3.2: Discussion of the results gathered using the eSDK.
- Section 3.3: Notes about potential issues of running code on the Epiphany.

3.1 COPRTHR

The results by using the COPRTHR SDK was fairly disappointing. If we can manage better performance from a standard ARM CPU that uses about the same amount of power, the selling point for the Epiphany is lost.

However, since the results using the eSDK is far better, we can see that the reason for the poor results here are not the CPU itself. The reason for the slow performance is unknown.

One reason for this may be that the COPRTHR SDK simply does not perform as well as the eSDK. Another reason may be that I could have used the COPRTHR SDK in a way that is not the most optimum for performance.

3.2 eSDK

Using the eSDK instead of the COPRTHR SDK yielded far better results. We can see that the Epiphany managed about 13 times better performance than a single thread of the Xilinx CPU when using arrays in the single threaded...
implementation. When allocating a memory space manually for the data in the single threaded implementation, the Epiphany ran about 17 times faster.

The reason for better performance using arrays than an allocated memory space is probably because of some optimizations by the C compiler. The implementation using a memory space is the one closest to the implementation for the Epiphany, so if we want to compare raw computational power, this is the relevant comparison.

However, in a normal C implementation we would probably use arrays and then get a bit better performance. This means that if we want to compare the performance gain of this algorithm, we would want to look at this comparison.

The Xilinx CPU on the Parallella is a fairly powerful processor in this category[13]. To see that we can gain many times better performance using the Epiphany, is very good.

Of course, the Xilinx CPU is a general processor, while the Epiphany is probably best suited for specific tasks. This means that the Epiphany will not replace the standard CPUs. It could mean that the Epiphany will be very interesting to use for the tasks that it is suited for.

The 64-core version of the Epiphany will have the same maximum power consumption as the version with 16-core used in this thesis[3][4]. In addition to having four times as many cores, they will also run on a slightly higher clock frequency. This means that this processor will have much higher performance, with about the same power consumption.

### 3.3 Programming issues

For the last part of the discussion, I would like to point out some issues I ran into, and some pointers to what is important to check.

It is very important that your environment is set up correctly. If you don’t have the correct environment variables set, you may not be able to run programs on the Epiphany. The error messages received because of this is not always obvious. All of the necessary environment variables are set up correctly in the run file provided in A.5.

If you try to read or write from an invalid memory segment on the Epiphany, for example if you write to an address that is not aligned to a word, the Epiphany may hang, without any feedback.
Chapter 4

Conclusion and Future work

In this project we have made implementations to test the performance of the Epiphany using the two SDKs available. The algorithms implementation and used for testing was vector multiplication, computation of the Mandelbrot set and Conway’s Game of Life. We saw that the performance of the Epiphany using the COPRTHR SDK was far worse than the main processor of the Parallella.

However, using the eSDK we saw that the performance of the Epiphany was 13–17 times better than the main processor. This result is considered very good. We also noted that there will come a version of the Epiphany with 64 cores which will have many times the performance of the version with 16 core, while still consuming about the same amount of power.

4.1 Future work

- As stated, there will come 64-core version of the Epiphany. When this is available, it would be interesting to test the performance of the processor compared to the version with 16 cores tested here.

- The power of this processor lies in its parallel nature. For problems that are far more parallel than for 16 or 64 cores, it would be interesting to see how the Epiphany would perform in a cluster. Either by networking many Parallella devices together, or even better, to combine many Epiphany processors on a single board. The goal could be to measure how this will perform in comparison with a super computer.

- The Epiphany is a processor with a very low power consumption. Maximum and typical power consumption are provided by Adapteva, but
this is very rough numbers. Another work could check the power consumption in detail for different computations, and compare this to other processors.

• Unlike GPUs, which has very many cores, each core of the Epiphany can do an individual computation simultaneously as all of the other cores. This means that the Epiphany could be well suited for task-parallel problems, as well as data-parallel problems. In this project, only data-parallel problems were explored. Another work could measure the performance of task-parallel problems using the Epiphany.
Bibliography


Appendix A

Code implementations

All of the code is also attached in a separate ZIP file.

```c
#include <stdio.h>
#include <stdlib.h>
#include <string.h>
#include <sys/time.h>

#define SIZE 1048576
#define SIZE16 "1048576 / 16"

#define SIZE 1048576
#define SIZE16 "1048576 / 16"

char src[] = 
"#include <coprthr.h>
"; \n
"void my_kern( float* a, float* b, float* c) \n"; \n
"int idx = coprthr_get_thread_index() * " SIZE16
"; \n
"int to = idx+" SIZE16 "; \n
"for (int i=idx; i<to; i++) \n"; \n
"c[i] = a[i]*b[i];\n"; \n
"}; \n

double walltime()
{
    static struct timeval t;
    gettimeofday(&t, NULL);
    return (t.tv_sec + 1e-6 * t.tv_usec);
}

int main()
{
    int i;
```
int dd = coprthr_dopen(COPRTHRDEVICE_E32, COPRTHR_O_STREAM);

if (dd<0) {
    printf("device open failed\n");
    exit(-1);
}

coprthr_program_t prg = coprthr_dcompile(dd, src, sizeof(src), ",", 0);
coprthr_kernel_t krn = coprthr_getsym(prg, "my_kern");

float* a = (float*) malloc(SIZE*sizeof(float));
float* b = (float*) malloc(SIZE*sizeof(float));
float* c = (float*) malloc(SIZE*sizeof(float));

for (i=0; i<SIZE; i++) {
    a[i] = 1.0f * i;
    b[i] = 2.0f * i;
    c[i] = 0.0f;
}

coprthr_mem_t mema = coprthr_dmalloc(dd, SIZE*sizeof(float), 0);
coprthr_mem_t memb = coprthr_dmalloc(dd, SIZE*sizeof(float), 0);
coprthr_mem_t memc = coprthr_dmalloc(dd, SIZE*sizeof(float), 0);

coprthr_dwrite(dd, mema, 0, a, SIZE*sizeof(float), COPRTHR_E_NOWAIT);
coprthr_dwrite(dd, memb, 0, b, SIZE*sizeof(float), COPRTHR_E_NOWAIT);
coprthr_dwrite(dd, memc, 0, c, SIZE*sizeof(float), COPRTHR_E_NOWAIT);

unsigned int nargs = 3;
void* args[] = { &mema, &memb, &memc };
unsigned int nthr = 16;

double runtime;
double start = walltime();
coprthr_dexec(dd, krn, nargs, args, nthr, 0, COPRTHR_E_NOWAIT);
coprthr_dcopy(dd, memc, 0, memb, 0, SIZE*sizeof(float), COPRTHR_E_NOWAIT);
#include <stdio.h>
#include <stdlib.h>
#include <string.h>
#include <sys/time.h>

#define SIZE 1048576

double walltime()
{
    static struct timeval t;
    gettimeofday(&t, NULL);
    return (t.tv_sec + 1e-6 * t.tv_usec);
}

int main()
{
    int i;
    
    float* a = (float*) malloc(SIZE*sizeof(float));
    float* b = (float*) malloc(SIZE*sizeof(float));
    float* c = (float*) malloc(SIZE*sizeof(float));
    
    for(i=0; i<SIZE; i++) {
        a[i] = 1.0f * i;
    }
    
    coprthr_dread(dd, memc, 0, c, SIZE*sizeof(float), COPRTHR_E_NOWAIT);
    coprthr_dwait(dd);
    
    runtime = walltime() - start;
    printf("Runtime: %8.3f ms\n", runtime*1e3);

    //for(i=0; i<SIZE; i++)
    //    printf("%f * %f = %f\n", a[i], b[i], c[i]);

    coprthr_dfree(dd, mema);
    coprthr_dfree(dd, memb);
    coprthr_dfree(dd, memc);
 |free(a);
 |free(b);
 |free(c);

coprthr_dclose(dd);

Listing A.1: Implementation of vector multiplication using COPRTHR


Listing A.2: Serial implementation of vector multiplication

/*
 * Originally created for CUDA by Ruben Spaans for Anne C Elster and her parallel programming class.
 * Modified to COPRTHR for the Parallela device by Trygve Aaberge.
 */

#include <stdio.h>
#include <stdlib.h>
#include <math.h>
#include <sys/time.h>
#include <string.h>
#include "coprthr.h"
#include "coprthr_cc.h"

/* Problem size */
#define XSIZE 16
#define YSIZE 16
#define MAXITER 255

double xleft = -2.01;
double xright = 1;
double yupper, ylower;

for (i = 0; i < SIZE; i++) {
    c[i] = a[i] * b[i];
}

runtime = walltime() - start;
printf("Runtime: %8.3f ms\n", runtime*1e3);

for (i=0; i<SIZE; i++)
    printf("%f + %f = %f \n", a[i], b[i], c[i]);

free(a);
free(b);
free(c);
double ycenter=1e-6;
double step;

int host_pixel[XSIZE*YSIZE];
int device_pixel[XSIZE*YSIZE];

typedef struct {
    double real,imag;
} my_complex_t;

#define PIXEL(i,j) ((i)+(j)*XSIZE)

void host_calculate()
{

}
for (int j=0; j<YSIZE; j++) {
    for (int i=0; i<XSIZE; i++) {
        /* Calculate the number of iterations
           until divergence for each pixel.
           If divergence never happens, return
           MAXITER */
        my_complex_t c,z,temp;
        int iter=0;
        c.real = (xleft + step*i);
        c.imag = (yupper - step*j);
        z = c;
        while(z.real*z.real + z.imag*z.imag < 4.0) {
            temp.real = z.real*z.real - z.imag*z.imag + c.real;
            temp.imag = 2.0*z.real*z.imag + c.imag;
            z = temp;
            if(++iter==MAXITER) break;
        }
        host_pixel[PIXEL(i,j)] = iter;
    }
}

typedef unsigned char uchar;

/* save 24−bits bmp file, buffer must be in bmp format: upside−down */
void savebmp(char *name, uchar *buffer, int x, int y)
{
    FILE *f=fopen(name,"wb");
    if(!f) {
        printf("Error writing image to disk.
") ;
        return;
    }
    unsigned int size=x*y*3+54;
    uchar header[54]=
        {'B','M',size&255,(size >>8)&255,(size >>16)&255, size >>24,0,        
        0,0,0,0,0,0,0,40,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0} ;
    fwrite(header,1,54,f);
    fwrite(buffer,1,x*y*3,f);
    fclose(f);
}

/* given iteration number, set a colour */
void fancycolour(uchar *p, int iter)
if (iter==MAXITER);
else if (iter<8) { p[0]=128+iter*16; p[1]=p[2]=0; }
else if (iter<24) { p[0]=255; p[1]=p[2]=(iter−8)*16; }
}
else { p[0]=p[1]=(iter−160)*2; p[2]=255−(iter−160)*2; }

Get system time to microsecond precision (ostensibly, the same as MPI_Wtime),
* returns time in seconds
*/

double walltime ()
{
    static struct timeval t;
    gettimeofday(&t, NULL);
    return (t.tv_sec + 1e−6 * t.tv_usec);
}

int main(int argc, char **argv)
{
    if (argc==1) {
        puts("Usage: MANDEL n");
        puts("n decides whether image should be written to disk (1=yes, 0=no)");
        return 0;
    }
    double start;
    double hosttime=0;
    double devicetime=0;
    double memtime=0;
    int dd = coprthr_dopen(COPRTHR_DEVICE_E32,
                           COPRTHR_O_STREAM);
    if (dd<0) {
        printf("device open failed\n");
        exit(-1);
    }
    coprthr_program_t prg = coprthr_dcompile(dd, src, sizeof(src),"",0);
    coprthr_kernel_t krn = coprthr_getsym(prg,"my_kern");
    */ Calculate the range in the y-axis such that we preserve the aspect ratio */

21
step = (xright - xleft) / XSIZE;
yupper = ycenter + (step * YSIZE) / 2;
ylower = ycenter - (step * YSIZE) / 2;

/* Host calculates image */
start = walltime();
host_calculate();
hosttime += walltime() - start;

/****** SUBTASK2: Set up device memory ***********/

coprthr_mem_t mema = coprthr_dmalloc(dd, XSIZE * YSIZE * sizeof(int), 0);
coprthr_mem_t memxleft = coprthr_dmalloc(dd, sizeof(double), 0);
coprthr_mem_t memyupper = coprthr_dmalloc(dd, sizeof(double), 0);
coprthr_mem_t memstep = coprthr_dmalloc(dd, sizeof(double), 0);

memset(device_pixel, 0, XSIZE * YSIZE * sizeof(int));
coprthr_dwrite(dd, mema, 0, device_pixel, XSIZE * YSIZE * sizeof(int), COPRTHR_E_NOWAIT);
coprthr_dwrite(dd, memxleft, 0, &xleft, sizeof(double), COPRTHR_E_NOWAIT);
coprthr_dwrite(dd, memyupper, 0, &yupper, sizeof(double), COPRTHR_E_NOWAIT);
coprthr_dwrite(dd, memstep, 0, &step, sizeof(double), COPRTHR_E_NOWAIT);

/****** SUBTASK2 END *****************************/

start = walltime();

/****** SUBTASK3: Execute the kernel on the device ***********/

unsigned int nargs = 4;
void* args[] = { &mema, &memxleft, &memyupper, &memstep };

unsigned int nthr = YSIZE;
coprthr_dexec(dd, krn, nargs, args, nthr, 0, COPRTHR_E_NOWAIT);
coprthr_dwait(dd);

/****** SUBTASK3 END *****************************/
devicetime+=walltime()-start;

start=walltime();
/******* SUBTASK4: Transfer the result from device to
device_pixel[][][]*/
coprthr_dread(dd,mema,0,device_pixel,XSIZE*YSIZE*sizeof(
    int),COPRTHR_E_NOWAIT);

coprthr_dwait(dd);

/******* SUBTASK4 END

memtime+=walltime()-start;

/******* SUBTASK5: Free the device memory also

*****************************************************************
coprthr_dfree(dd,mema);

/******* SUBTASK5 END

*****************************************************************

int errors=0;
/* check if result is correct */
for(int i=0;i<XSIZE;i++) {
    for(int j=0;j<YSIZE;j++) {
        int diff=host_pixel[PIXEL(i,j)]-
    device_pixel[PIXEL(i,j)];
        if(diff<0) diff=-diff;
        /* allow +1 difference */
        if(diff>1) {
            if(errors<10)
                printf("Error on pixel %d,%d: expected %d, found %d\n",i,j,host_pixel[PIXEL(i,j)],
device_pixel[PIXEL(i,j)]);
            else if(errors==10)
                puts("...");
        errors++;
    }
}
if(errors>0) printf("Found %d errors.\n",errors);
else puts("Device calculations are correct.");
printf("\n");
printf("Host time: \%7.3f ms\n", hosttime*1e3);
printf("Device calculation: \%7.3f ms\n", devicetime*1e3);
printf("Copy result: \%7.3f ms\n", memtime*1e3);

if (strtol(argv[1], NULL, 10) != 0) {
    /* create nice image from iteration counts. take care to create it upside down (bmp format) */
    unsigned char *buffer = (unsigned char *) alloc(XSIZE*YSIZE*3, 1);
    for (int i = 0; i < XSIZE; i++) {
        for (int j = 0; j < YSIZE; j++) {
            int p = ((YSIZE-j-1)*XSIZExSIZE+i)*3;
            fancycolour(buffer+p, device_pixel[PIXEL(i, j)]);
        }
    }
    /* write image to disk */
    savebmp("mandel1.bmp", buffer, XSIZE, YSIZE);
}
return 0;

Listing A.3: Serial implementation and implementation using COPRTHR of mandelbrot

ESDK = $(EPYPHANY_HOME)
ELIBS = $(ESDK)/tools/host/lib
EINCS = $(ESDK)/tools/host/include
ELDF = $(ESDK)/bsps/current/internal.ldf
EXES = main epiphany.srec
OBJS = epiphany.elf

all: $(EXES)
main: host.c shared_data.h
    gcc -O3 host.c -o main -I $(EINCS) -L $(ELIBS) -le-hal -lrt
epiphany.elf: epiphany.c shared_data.h
    e-gcc -O3 -funroll-loops -ffast-math -T $(ELDF) epiphany.c
        -o epiphany.elf -le-lib
epiphany.srec: epiphany.elf
    e-objcopy --srec --forceS3 --output-target srec epiphany.elf epiphany.srec
```bash
#!/bin/bash

set -e

ESDK=${EPIPHANY_HOME}
ELIBS=${ESDK}/tools/host/lib:${LD_LIBRARY_PATH}
EHDF=${EPIPHANY_HDF}

clean:
    rm $(EXES) $(OBJS)

Listing A.4: Makefile for implementations using eSDK

Listing A.5: Run-file for implementations using eSDK

/*
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Originally code for Mandelbrot computation, licenced as stated
above, from:
https://github.com/parallella/parallella-examples/tree/master/
mandelbrot
Used as basis and modified for this Game of Life implementation.
*

#include <stdio.h>
#include <string.h>
#include <stdlib.h>
#include <unistd.h>
#include <fcntl.h>
#include <sys/ioctl.h>
#include <sys/mman.h>
#include <stdint.h>
#include <time.h>
#include <e-hal.h>
#include "shared_data.h"
#define BUF_OFFSET 0x01000000

static inline void nano_wait(uint32_t sec, uint32_t nsec)
{
    struct timespec ts;
    ts.tv_sec = sec;
    ts.tv_nsec = nsec;
    nanosleep(&ts, NULL);
}

int main(int argc, char *argv[])
{
    e_platform_t eplat;
    e_epiphany_t edev;
    e_mem_t emem;
static msg_block_t msg;
memset(&msg, 0, sizeof(msg));
struct timespec time;
double time0, time1;
e_init(NULL);
e_reset_system();
e_get_platform_info(&eplat);
e_alloc(&emem, BUF_OFFSET, sizeof(msg_block_t));
unsigned int xres = XRES;
unsigned int yres = YRES;
msg.info.xres = xres;
msg.info.yres = yres;
unsigned int core = 0;
unsigned int row = 0;
unsigned int col = 0;
volatile unsigned int vepiphany[CORES];
volatile unsigned int vcoreid = 0;
unsigned int vhost[CORES];
for (core = 0; core < CORES; core++) {
    vepiphany[core] = 0;
    vhost[core] = 0;
}
FILE *file = fopen("grid.txt", "r");
if (file == NULL) {
    fprintf(stderr, "Could not open grid.txt\n");
    exit(1);
}
int x, y, c;
int eol = 0;
for (row = 0; row < ROWS; row++) {
    for (y = 0; y < yres; y++) {
        for (col = 0; col < COLS; col++) {
            core = row * COLS + col;
            for (x = 0; x < xres; x++) {
                if (!eol) {
                    c = fgetc(file);
                    if (c == '\n') {
                        eol = 1;
                    }
                } else {
                    msg.pixels[core][y][x] = 1;
                }
            }
        }
    }
}
msg.pixels[core][y][x] = 0;

{  
    }  
  
eol = 0;

}
e_open(&edev, 0, 0, ROWS, COLS);
e_write(&emem, 0, 0, &msg, sizeof(msg));
e_reset_group(&edev);
e_load_group("epiphany.srec", &edev, 0, 0, ROWS, COLS, E_TRUE);
nano_wait(0, 100000000);
clock_gettime(CLOCK_REALTIME, &time);
time0 = time.tv_sec + time.tv_nsec * 1.0e-9;

unsigned int frame = 0;
while (1) {
    for (row = 0; row < ROWS; row++) {
        for (col = 0; col < COLS; col++) {
            core = row * COLS + col;
            while (1) {
                e_read(&emem, 0, 0, (off_t)((char *)&msg.
msg_d2h[core] - (char *)&msg).
msg_d2h[core], sizeof
(msg_dev2host_t));
vepiphany[core] = msg.
msg_d2h[core].value;
if (vhost[core] -
vepiphany[core] >
)((~0u) >> 1)) {
                break;
            }
            nano_wait(0, 10000000);
        }
    
}  
}  
vhost[core] = vepiphany[core];
vcoreid = msg.msg_d2h[core].
coreid;
//printf("%x row:%d col:%d\n",
vepiphany[core], (vcoreid >>
6), (vcoreid & 0x3f));

}  
}  
for (row = 0; row < ROWS; row++) {
    for (col = 0; col < COLS; col++) {

for (core = 0; core < CORES; core++) {
    while (1) {
        e_read(&emem, 0, 0, (off_t)((char *)&msg.
         msg_d2h[core] - (char *)&msg), &msg.
         msg_d2h[core], sizeof(msg_dev2host_t)) ;
        if (msg.msg_d2h[core].finished == 1) {
            break ;
        }
        nano_wait(0, 1000000);
        e_read(&emem, 0, 0, (off_t)((char *)&msg.pixels[
         core] - (char *)&msg), &msg.pixels[core],
         sizeof(uint32_t[yres][xres])) ;
    }
}

clock_gettime(CLOCK_REALTIME, &time);
time1 = time.tv_sec + time.tv_nsec * 1.0e-9;
printf("rows: %d, cols: %d\n", ROWS * yres, COLS * xres);
printf("frames: %d\n", FRAMES);
printf("time: %f sec\n", time1 - time0);

for (row = 0; row < ROWS; row++) {
    for (y = 0; y < yres; y++) {
        for (col = 0; col < COLS; col++) {
            core = row * COLS + col;
            for (x = 0; x < xres; x++) {
                printf("%d ", msg.pixels[
                    core][y][x]);
            }
        }
    }
    printf("\n");
}
e_close(&edev);
e_free(&emem);
e_finalize();
return 0;
Listing A.6: Implementation of Game of Life using eSDK, host.c

```c
/*
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```
Originally code for Mandelbrot computation, licenced as stated above, from:
https://github.com/parallel-la/parallel-la-examples/tree/master/mandelbrot

Used as basis and modified for this Game of Life implementation.

*/

#include "e_lib.h"
#include "shared_data.h"

#define BUF_ADDRESS 0x8f000000
#define PAGE_OFFSET 0x2000
#define PAGE_SIZE 0x2000

unsigned int row, col, core;
unsigned int xres, yres;

unsigned int fetch_node(char *src, int x, int y, int x_diff, int y_diff) {
    int get_global = 0;
    int col_new = col;
    int row_new = row;
    int x_new = x + x_diff;
    if (x_new < 0) {
        col_new--;
        if (col_new < 0) {
            return 0;
        }
        x_new = xres - 1;
        get_global = 1;
    } else if (x_new == xres) {
        col_new++;
        if (col_new >= COLS) {
            return 0;
        }
    }
    x_new = 0;
    get_global = 1;

    int y_new = y + y_diff;
    if (y_new < 0) {
        row_new--;
        if (row_new < 0) {
            return 0;
        }
        y_new = yres - 1;
        get_global = 1;
    } else if (y_new == yres) {
        row_new++;
    }
    return src[row * xres + col];
}
if (row_new >= ROWS) {
    return 0;
}

y_new = 0;
global = 1;
}

char *src2 = src + (y_new * yres + x_new) * 4;
if (get_global) {
    src2 = e_get_global_address(row_new, col_new, src2);
}

return *src2;
}

int main(void)
{
    e_coreid_t coreid;
    coreid = e_get_coreid();
    e_coords_from_coreid(coreid, &row, &col);
    unsigned int frame = 1;
    unsigned int page = 1;
    volatile msg_block_t *msg = (msg_block_t *)BUF_ADDRESS;
    xres = msg->info.xres;
    yres = msg->info.yres;

    // For simplicity, each value is stored in a word. Since
    // a word is 4 bytes,
    // and the only possible values are 0 and 1, it would be
    // possible to store 32
    // values for each word.

    char *src[2] = {
        (char *) (PAGE_OFFSET),
        (char *) (PAGE_OFFSET + PAGE_SIZE)
    };
    unsigned int *pixel_last = (unsigned int *) src[0];
    unsigned int *pixel_cur = (unsigned int *) src[1];
    unsigned int x, y;
    for (y = 0; y < yres; y++) {
        for (x = 0; x < xres; x++) {
            *pixel_last = msg->pixels[core][y][x];
            *pixel_cur = 0;
            pixel_last++;
            pixel_cur++;
        }
    }

    while (1) {
        msg->msg_d2h[core].coreid = coreid;
    }
msg->msg_d2h[core].value = frame;

_asm__ __volatile__ ("trap 4");

pixel_cur = (unsigned int *)src[page];
page = page ^ 1;

if (frame > FRAMES) {
    break;
}
frame++;

for (y = 0; y < yres; y++) {
    for (x = 0; x < xres; x++) {
        int is_live = fetch_node(src[page], x, y, 0, 0);
        int nr_live = 0;
        nr_live += fetch_node(src[page], x, y, -1, -1);
        nr_live += fetch_node(src[page], x, y, -1, 0);
        nr_live += fetch_node(src[page], x, y, -1, 1);
        nr_live += fetch_node(src[page], x, y, 0, -1);
        nr_live += fetch_node(src[page], x, y, 0, 1);
        nr_live += fetch_node(src[page], x, y, 1, -1);
        nr_live += fetch_node(src[page], x, y, 1, 0);
        nr_live += fetch_node(src[page], x, y, 1, 1);
        if (is_live) {
            if (nr_live < 2 || nr_live > 3) {
                *pixel_cur = 0;
            } else {
                *pixel_cur = 1;
            }
            } else if (nr_live == 3) {
                *pixel_cur = 1;
            } else {
                *pixel_cur = is_live;
            }
    }
    pixel_cur++;
}
pixel_cur = (unsigned int *)src[page];
for (y = 0; y < yres; y++) {
    for (x = 0; x < xres; x++) {
        msg->pixels[core][y][x] = *pixel_cur;
        pixel_cur++;
    }
}

msg->msg_d2h[core].finished = 1;
return 0;

Listing A.7: Implementation of Game of Life using eSDK, epiphany.c
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above, from:
https://github.com/parallella/parallella-examples/tree/master/
mandelbrot
Used as basis and modified for this Game of Life implementation.
*/

#include <stdint.h>
#define FRAMES 100
#define ALIGN8 8
#define CORES 16
#define ROWS 4
#define COLS 4
#define XRES 45
#define YRES 45

typedef struct __attribute__((aligned(ALIGN8))) {
    uint32_t value;
    uint32_t coreid;
    uint32_t finished;
} msg_dev2host_t;

typedef struct __attribute__((aligned(ALIGN8))) {
    uint32_t value;
} msg_host2dev_t;

typedef struct __attribute__((aligned(ALIGN8))) {
    uint32_t xres;
    uint32_t yres;
} info_t;

typedef struct {
    msg_host2dev_t msg_h2d[CORES];
    msg_dev2host_t msg_d2h[CORES];
    info_t info;
Listing A.8: Implementation of Game of Life using eSDK, shared_data.h

```c
#define FRAMES 100
#define XRES 180
#define YRES 180

unsigned int xres, yres;
unsigned int pixels[2][YRES][XRES];

static inline void nano_wait(uint32_t sec, uint32_t nsec)
{
    struct timespec ts;
    ts.tv_sec = sec;
    ts.tv_nsec = nsec;
    nanosleep(&ts, NULL);
}

unsigned int fetch_node(unsigned int page, int x, int y, int x_diff, int y_diff) {
    int x_new = x + x_diff;
    if (x_new < 0 || x_new >= xres) {
        return 0;
    }
    int y_new = y + y_diff;
    if (y_new < 0 || y_new >= yres) {
        return 0;
    }
    return pixels[page][y_new][x_new];
}

int main(int argc, char *argv[]) {
    struct timespec time;
    double time0, time1;
    xres = XRES;
    yres = YRES;
```
unsigned int row = 0;
unsigned int col = 0;

FILE *file = fopen("grid.txt", "r");
if (file == NULL) {
    fprintf(stderr, "Could not open grid.txt\n");
    exit(1);
}

int x, y, c;
int col = 0;
for (y = 0; y < yres; y++) {
    for (x = 0; x < xres; x++) {
        if (!col) {
            c = fgetc(file);
            if (c == '\n') {
                col = 1;
            }
        }
        if (c == '1') {
            pixels[0][y][x] = 1;
        } else {
            pixels[0][y][x] = 0;
        }
        pixels[1][y][x] = 0;
        col = 0;
    }
}

clock_gettime(CLOCK_REALTIME, &time);
time0 = time.tv_sec + time.tv_nsec * 1.0e-9;

unsigned int frame = 1;
unsigned int page_last = 0;
unsigned int page_cur = 1;
while (1) {
    if (frame > FRAMES) {
        break;
    }
    frame++;

    for (y = 0; y < yres; y++) {
        for (x = 0; x < xres; x++) {
            int is_live = fetch_node(
                page_last, x, y, 0, 0);
            int nr_live = 0;
            nr_live += fetch_node(page_last, x, y, -1, -1);
nr_live += fetch_node(page_last, x, y, -1, 0);
nr_live += fetch_node(page_last, x, y, -1, 1);
nr_live += fetch_node(page_last, x, y, 0, -1);
nr_live += fetch_node(page_last, x, y, 0, 1);
nr_live += fetch_node(page_last, x, y, 1, -1);
nr_live += fetch_node(page_last, x, y, 1, 0);
nr_live += fetch_node(page_last, x, y, 1, 1);

if (is_live) {
    if (nr_live < 2 || nr_live > 3) {
        pixels[page_cur][y][x] = 0;
    } else {
        pixels[page_cur][y][x] = 1;
    }
} else if (nr_live == 3) {
    pixels[page_cur][y][x] = 1;
} else {
    pixels[page_cur][y][x] = is_live;
}

page_last = page_cur;
page_cur = page_cur ^ 1;
}

(clock_gettime(CLOCK_REALTIME, &time);
time1 = time.tv_sec + time.tv_nsec * 1.0e-9;
printf("rows: %d, cols: %d\n", yres, xres);
printf("frames: %d\n", FRAMES);
printf("time: %f sec\n", timel - time0);

for (y = 0; y < yres; y++) {
    for (x = 0; x < xres; x++) {
        printf("%d ", pixels[page_last][y][x]);
    }
    printf("\n");
}
return 0;
}

Listing A.9: Serial implementation of Game of Life, using arrays

```c
#include <stdio.h>
#include <string.h>
#include <stdlib.h>
#include <unistd.h>
#include <fcntl.h>
#include <sys/ioctl.h>
#include <sys/mman.h>
#include <stdint.h>
#include <time.h>

#define FRAMES 100
#define XRES 180
#define YRES 180

unsigned int xres, yres;

static inline void nano_wait(uint32_t sec, uint32_t nsec)
{
    struct timespec ts;
    ts.tv_sec = sec;
    ts.tv_nsec = nsec;
    nanosleep(&ts, NULL);
}

unsigned int fetch_node(char *src, int x, int y, int x_diff, int y_diff)
{
    int x_new = x + x_diff;
    if (x_new < 0 || x_new >= xres) {
        return 0;
    }
    int y_new = y + y_diff;
    if (y_new < 0 || y_new >= yres) {
        return 0;
    }
    char *src2 = src + (y_new * yres + x_new) * sizeof(unsigned int);
    return *src2;
}

int main(int argc, char *argv[])
{
    struct timespec time;
    double time0, time1;
    ```
xres = XRES;
yres = YRES;

unsigned int row = 0;
unsigned int col = 0;

char *src[2] = {
    malloc(xres * yres * sizeof(unsigned int)),
    malloc(xres * yres * sizeof(unsigned int))
};

FILE *file = fopen("grid.txt", "r");
if (file == NULL) {
    fprintf(stderr, "Could not open grid.txt\n");
    exit(1);
}

int x, y, c;
int eol = 0;
unsigned int *pixel_last = (unsigned int *)src[0];
unsigned int *pixel_cur = (unsigned int *)src[1];
for (y = 0; y < yres; y++) {
    for (x = 0; x < xres; x++) {
        if (!eol) {
            c = fgetc(file);
            if (c == '\n') {
                eol = 1;
            }
        } else {
            *pixel_last = 0;
        }
        *pixel_cur = 0;
        pixel_last++;
        pixel_cur++;
    }
    eol = 0;
}

clock_gettime(CLOCK_REALTIME, &time);
time0 = time.tv_sec + time.tv_nsec * 1.0e-9;

unsigned int frame = 1;
unsigned int page = 1;
while (1) {
    pixel_cur = (unsigned int *)src[page];
    page = page ^ 1;
if (frame > FRAMES) {
    break;
}
frame++;

for (y = 0; y < yres; y++) {
    for (x = 0; x < xres; x++) {
        int is_live = fetch_node(src[page], x, y, 0, 0);
        int nr_live = 0;
        nr_live += fetch_node(src[page], x, y, -1, -1);
        nr_live += fetch_node(src[page], x, y, -1, 0);
        nr_live += fetch_node(src[page], x, y, -1, 1);
        nr_live += fetch_node(src[page], x, y, 0, -1);
        nr_live += fetch_node(src[page], x, y, 0, 1);
        nr_live += fetch_node(src[page], x, y, 1, -1);
        nr_live += fetch_node(src[page], x, y, 1, 0);
        nr_live += fetch_node(src[page], x, y, 1, 1);
        if (is_live) {
            if (nr_live < 2 || nr_live > 3) {
                *pixel_cur = 0;
            } else {
                *pixel_cur = 1;
            }
        } else if (nr_live == 3) {
            *pixel_cur = 1;
        } else {
            *pixel_cur = is_live;
        }
        pixel_cur++;
    }
}

clock_gettime(CLOCK_REALTIME, &time);
timel = time.tv_sec + time.tv_nsec * 1.0e-9;
printf("rows: %d, cols: %d\n", yres, xres);
printf("frames: %d\n", FRAMES);
Listing A.10: Serial implementation of Game of Life, using a memory space

130 printf("time: %f sec\n", time1 - time0);
131
132 pixel_cur = (unsigned int *)src[page];
133 for (y = 0; y < yres; y++) {
134     for (x = 0; x < xres; x++) {
135         printf("%d ", *pixel_cur);
136         pixel_cur++;
137     }
138     printf("\n");
139 }
140
141 free(src[0]);
142 free(src[1]);
143
144 return 0;
145 }