A Method for Model-Driven Information Flow Security

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ABSTRACT
We present a method for software development in which information flow security is taken into consideration from start to finish. Initially, the user of the method (i.e., a software developer) specifies the system architecture and selects a set of security requirements (in the form of secure information flow properties) that the system must adhere to. The user then specifies each component of the system architecture using UML inspired state machines, and refines/transforms these (abstract) state machines into concrete state machines. It is shown that if the abstract specification adheres to the security requirements, then so does the concrete one provided that certain conditions are satisfied.
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Abstract

We present a method for software development in which information flow security is taken into consideration from start to finish. Initially, the user of the method (i.e., a software developer) specifies the system architecture and selects a set of security requirements (in the form of secure information flow properties) that the system must adhere to. The user then specifies each component of the system architecture using UML inspired state machines, and refines/transforms these (abstract) state machines into concrete state machines. It is shown that if the abstract specification adheres to the security requirements, then so does the concrete one provided that certain conditions are satisfied.

1 Introduction

Security incidents occur on a daily basis within many companies. In CSI/FBI Computer Crime and Security Survey for 2005 [9], 74% of the companies reported security incidents. Despite the importance of security, careful engineering of security into overall design is often neglected and security features are typically built into an application in an ad-hoc manner or are only integrated during the final phases of system development [22].

Model-Driven Security (MDS) [8] advocates the opposite. MDS aims to raise the level of abstraction in design and development of secure systems by supporting (1) a model-driven development process in which security is taken into account from start to finish, (2) a clear separation of abstract, platform independent models (PIMs) and refined, platform specific models (PSMs), and (3) adherence preserving transformations between PIMs and PSMs.

The central idea of MDS is that systems can be specified and shown to be in adherence with security requirements at different levels of abstraction. Abstraction is believed to simplify analysis, facilitate reuse of designs and early discovery of design flaws. At each level of abstraction, we distinguish between a system specification and a set of security requirements the system specification must adhere to. When we have established that a system specification adheres to the security requirements at a given level, this relationship should also hold
at the next level so that the invested effort to establish adherence is not wasted. Hence, we would like adherence to be preserved under transformation. Adding details to a specification may of course require some additional analysis. However, it should not be necessary to recheck the adherence relationship already established at the more abstract level.

The MDS framework is illustrated in Fig.1. Here a platform independent model together with its security requirements are transformed into several platform specific models with associated security requirements.

Already published approaches to MDS include [2, 3, 5, 8, 10, 14, 22, 37]. Although interesting, they are in most cases of a rather informal nature; the semantics of the languages used (at abstract or/and concrete levels) are not sufficiently precise to allow for rigorous reasoning at more than one level of abstraction. Moreover, some of the approaches ([2, 3, 8, 22]) consider transformation of security requirements only, and not transformation of system specifications. These approaches allow adherence checking only at the lowest level of abstraction. Others ([5, 8, 10, 37]) do not clearly characterize what it means for a system to adhere to a security requirement. Instead, security is described in terms of a security mechanism.

Security is often defined as the preservation of confidentiality, integrity, and availability [15]. In this report, we, however, focus on security in the more narrow sense of secure information flow properties (see e.g., [4, 11, 25, 27, 30, 31, 35, 38]) which provide an elegant way of specifying confidentiality as well as integrity requirements [26].

The notion of transformation is closely related to refinement. That is, refinement is the (possibly manual) process of making an abstract specification more concrete, whereas transformation typically is a special case of refinement in which this process is automatic. There are several kinds of refinement. One of the refinement notions considered in this report, is refinement w.r.t. under-specification, i.e., the process of removing alternative design choices that are equivalent in the sense that it suffices for an implementation to provide only of them. In the classical literature, this kind of refinement is often referred to as behavioral refinement or property refinement [6]. It has long been recog-
nized that secure information flow properties are not preserved under standard definitions of refinement w.r.t. underspecification [16]. We avoid this problem by defining refinement in a semantic framework which is more expressive than conventional frameworks. In our method, system specifications are written in a state machine notation inspired by UML. But contrary to UML state machines, we have two constructs of choice; one describing alternative design choices, and one describing choices which should be provided by the system. The distinction between the two kinds of choices is necessary in order to handle refinement of secure information flow properties [13, 17, 31].

Software systems are almost never built entirely from scratch, i.e., a set of already implemented operations is usually available from the operating systems, runtime environments, or from libraries of programming environments. To take this into account, our state machines are allowed to reference events that are already available in a predefined event library. These events are later substituted by their definitions by a so-called event transformation. The transformation may therefore be understood as a special case of what in the literature is known as action refinement [36]. We provide a formal characterization of the syntax and semantics of transformations that are induced by event libraries, and define general conditions under which transformations induced by event libraries preserve arbitrary secure information flow properties.

This report is structured as follows: Sect. 2 gives an overview of our method. In Sect. 3 - 9 the individual steps of our method are presented. Sect. 10 discusses related work, and Sect. 11 provides conclusions and directions of future work. In the appendix, we formally define state machines (App. A) and event transformations (App. B). A glossary of symbols is provided in App. C and proofs are presented in App. D.
2 Overview of method

The goal of our method for model-driven information flow security is to support the specification and development of secure software systems. As illustrated in Fig. 2, the method has seven main steps.

In step I, the user of our method (i.e., a software developer) specifies the system architecture using UML composite structures. The system architecture is an overview of the components of the system and their associated communication channels. The user then partitions the system into security domains by labeling the system architecture with security relevant annotations.

In step II, the user selects a set of secure information flow properties (typically from a library) that the specification must adhere to. The secure information flow properties of the library are assumed to be defined in our security property schema [33, 34] which ensures that the adherence to the properties is preserved when design decisions are resolved by refinement in later steps.

In step III, the user specifies each component of the system architecture using our UML inspired state machines. The state machine notation provides constructs for specifying both design choices and choices that must be offered by the components of the system. The specified state machines may reference events that are already provided in a predefined event library. In this step, new event specifications may also be uploaded to the event library.

In step IV, the user verifies that the state machine based specification adheres to the security properties selected in step II. There are many techniques and methods that can be used for this purpose. We do not go into details on these. However, we provide a precise characterization of what it means for a system specification to adhere to a secure information flow property. This characterization provides a formal foundation for adherence verification.

In step V, the user refines the specification by removing alternative design decisions until all design decisions are decided. The validity of adherence is guaranteed to be preserved under this kind of refinement.

In step VI, the state machine specification of step V is transformed into a more concrete specification by substituting the event references of the specification by their definitions in the event library. We give a formal characterization of the syntax and semantics of these so-called event transformations, and show that they satisfy some desirable properties.

In step VII, the user verifies that the event transformation of step VI preserves adherence to the security properties selected in step II. We present conditions that can be used to check that the transformation preserves adherence.

In the MDS terminology, the specification produced in step V may be seen as a platform independent model (PIM), whereas the state machine produced in step VII may be seen as a platform specific model (PSM). Note that although a fully general approach to MDS would consider the transformation of both (abstract) security properties and system specifications into (concrete) security properties and system specifications, we only consider the transformation of the system specifications. The reason for this is that the security properties we consider are essentially parameterized w.r.t. to the different levels of abstraction. Hence, there is normally no need to transform the actual specification of the properties.
3 Step I: Specify and annotate the system architecture

In step I of our method, the user specifies the system architecture, i.e., an overview of the basic state machines that the system specification consists of and their associated communication channels. We will often refer to the architecture specification as a composite state machine. After the architecture has been specified, the user partitions the system into security domains by annotating the specification. In the following, we introduce a running example which will be used to explain this step and the subsequent steps of our method.

Consider a large software developing company that aims to develop a distributed system, the project management system (the PM system), in order to centralize all storage of software development projects. Software developers should be able to retrieve projects from a server to their local machines, edit or add files to the project, and upload any changes back to the server.

Currently, the company has no unified development method, and developers working on different projects are to a large degree given flexibility in the method they choose to adopt. The company wants to assess the different methods in order to recommend improvements, and possibly to introduce a unified development process. This task is assigned to a group of researchers. The researchers are to pick a set of sample projects, and assess each project thoroughly with respect to progress, quality of code etc. For convenience, the PM system should be augmented slightly such that the researchers will be able to retrieve projects from the server over the Internet on a regular basis. This additional functionality is not of high priority, thus it will be implemented with little resources. It is not initially known whether the researchers should use the same client as the developers.

To make the assessment as realistic as possible, the developers should not know which projects the researchers have sampled for the assessment. As we will see later, this requirement may be enforced by selection an appropriate secure information flow property.

Fig. 3 shows the architecture specification of the PM system with one client acting on the behalf of the developer (ClientD), one client acting on the behalf of the researcher (ClientR), and one server (Server). The architecture specification is intended to give an overview of the components or basic state machines that the system specification consists of. As a graphical notation, we have chosen to use UML composite structures (see App. A for more details).

After the system architecture has been specified, the user partitions the architecture into security domains by annotating the components with the security domain they belong to. In our running example, developers are not allowed to find out which projects the researchers have sampled for the assessment. Thus
we let the developers belong to one security domain which we call the low level domain, and the researchers belong to another domain, which we call the high level domain. We require that information should not flow from the high level domain to the low level domain.

In Fig. 4, the domains that the developer and the researcher clients belong to are specified by the labels <<Low>> and <<High>>, respectively. For short, we denote the low level domain by \( L \), and the high level domain by \( H \). Semantically, we interpret a security domain by a set of events, where an event represents the transmission or the reception of a message. In the current example, this means that \( L \) denotes all events that represent messages that can be sent to or from ClientID, whereas \( H \) denotes all events that represent messages that can be sent to or from ClientR.

4 Step II: Select security properties

In step II, the user selects a set of secure information flow properties, referred to as security properties for short, that the system must adhere to. A security property defines what it means that information flows from one security domain to another.

The security requirement of the running example may be formalized by the security property non-inference [36] denoted \( \text{NF} \). This is one of the most well-known security properties of the literature. It treats all the behavior of the high level domain \( H \) (the behavior of the researchers in this example) as confidential, and requires that the low level user (the developer) must not deduce that any event in \( H \) has occurred.

We assume (1) that the developer may observe all events in \( L \), i.e., its own communication with the server and (2) that the developer has complete knowledge of the system specification. Therefore, for each observation (i.e., a sequence of events in \( L \)) that the developer can make by interacting with the server, the developer can look at the specification and construct the so-called low level equivalence set of all traces (i.e., sequences of events describing system executions) that are compatible with that observation. The developer will know that one of the traces in this set has occurred, but not which one. However, if all the traces of this set include a high level event in \( H \), then the developer can conclude with certainty that the researcher has done something. In this case, the specification would not be secure w.r.t. the NF property. Conversely, if there is one trace in the low level equivalence set which does not include a high level event, then the developer cannot conclude with certainty that the researcher has done something.

In general, the underlying idea of all secure information flow properties is to demand that each low level equivalence set must contain a trace which prevents
the low level user from deducing that some confidential behavior has (or has not) occurred.

The above discussion suggests that all security properties have two essential ingredients:

- a definition of low level equivalence;
- a definition of the high level behavior which should be regarded as confidential.

Formally, two traces \( s \) and \( t \) are low level equivalent, written \( s \sim t \), if they contain the same sequences of low level events, i.e.,

\[
s|_L = t|_L
\]

where \( t|_L \) yields the trace obtained from \( t \) by removing all events not in the set of events \( L \). The definition of low level equivalence is the same for all security properties. The definition of confidential behavior, however, differs depending on the property. For the \( \text{Nf} \) property – which treats traces that contain any high level event in \( H \) as confidential – the set of confidential behavior \( C \) may be defined by the following predicate

\[
C(t) \equiv t|_H \neq ()
\]

where () denotes the empty trace. In other words, a trace is confidential if it is non-empty when all non high level events have been removed from it.

For a system \( S \), whose set of possible traces is denoted by \( \llbracket S \rrbracket \), to adhere to the \( \text{Nf} \) property, there must, for each low level observation that can be made from \( \llbracket S \rrbracket \), be a trace in \( \llbracket S \rrbracket \) which is not confidential (i.e., not included in \( C \)) and which is low level equivalent to the observation. Formally we have

\[
\text{Nf}(\llbracket S \rrbracket) \equiv \forall t \in \llbracket S \rrbracket : \exists u \in \llbracket S \rrbracket : \neg C(u) \land u \sim t
\]

All security properties are of a similar form as (3). In fact, \( \text{Nf} \) may be expressed as an instance of a security predicate schema which is parameterized by a predicate which characterizes confidential behavior (see [34, 33]). We assume that all security properties that can be selected by the user in step II are instances of the security property schema.

5 Step III: Specify system components

In step III of our method, the user specifies each basic state machine of the architecture. It is assumed that a library of predefined event specifications is available to the user. These events can be referenced in the state machine specifications.

Fig. 5 shows the specification of the two clients ClientD and ClientR. ClientD receives an input document from its user (not shown), and passes that document on to the server by sending the message storeDoc. In the state machine notation, the black circle represents the initial state and the boxes with rounded edges represent standard states. Transitions are specified by arrows between states. Transitions may be labeled by action expressions of the form \( \text{nm.si}(bx)/ef \), where \( \text{nm.si} \) is an input event, \( [bx] \) (where \( bx \) is a boolean expression) is a
Figure 5: Specification of the clients

guard, and ef is an effect. Event triggers, guards, and effects are all optional in the expression.

Intuitively, if the current state of the state machine is q, and the state machine has a transition from q to state q' that is labeled by nm.si[kz]/ef, then the state machine will set its current state to q' if it receives signal si from the basic state machine whose name is nm and the guard kz is evaluated to true. The effect ef will be executed when the state machine moves to q'.

There are two kinds of effects: output events and assignments. An output event is an expression of the form nm.si that represents the transmission of a signal si to the state machine whose name is nm. Assignments are denoted by expressions of the form x = ez where x is a variable and ez is a term built from common basic types such as booleans, integers, and strings, and common operations for these.

The graphical notation used for specifying state machines is essentially a subset of the UML state machine notation. See App. A for more details.

We assume that the message transmission protocol between the server and the client is provided by the event library. As specified in Fig. 5, the clients invoke the action P1 (for protocol 1) in order to send messages to the server.

The client acting on the behalf of the researcher (ClientR) is similar to ClientD, except for two differences. First, the researcher retrieves documents from the server instead of storing them, and second ClientR has the choice of either using protocol P1 or using protocol P2. This is indicated by the alt-construct which is used to specify design choices that are potential in the sense that one of the choice alternatives can be removed during refinement. In the current example, the design choice is specified because it is not initially known whether or not ClientR will use the same protocol as ClientD.

The specification of the server is shown in Fig. 6. Upon receiving the message storeDoc (resp. getDoc) from the client, it sends the message storeDocument (resp. retrieveDoc) to a database (not shown) which stores (resp. retrieves) the document. The server, moves into a state in which it waits for an ok message from the database. If the server receives any message from the clients while waiting for the data base, then these messages are simply dropped. As indicated by the alt-construct, the protocol used in order to transmit and receive messages to the researcher client is an open design choice.
The denotational semantics of standard state machines is usually given as the set of traces that is obtained by recording the events of all paths in the state machine when starting from the initial state. However, we distinguish between two kinds of choices, and interpret state machines as sets of trace sets called obligations. Intuitively, the traces of an obligation are equivalent in the sense that an implementation is only required to produce one of them. Thus, each trace of an obligation represent underspecification, or potential choices. However, an implementation is required to produce at least one trace in each obligation. Thus obligations represent explicit choices that have to be present in an implementation.

The obligations described by a basic state machine \( P \) is denoted by \( \{ P \} \) (see App. A.3 for a formal definition). Choices that are not potential will result in new obligations being created, whereas potential choices will result in obligations being collapsed. For instance, the semantics of ClientD is

\[
\{ \} \{ \{ u.i, l.s.p1 \}, \{ u.i, l.s.p1, u.i, l.s.p1 \} \}
\]

Here a new obligation is created for each finite iteration of the loop in ClientD. Each obligation consists of a single trace since there are no potential choices in ClientD. Note that state machine names and signal names have been shortened, and that expressions of the form \( ? u.m.s \) and \( ! m.s.i \) represent input and output events, respectively.

The semantics of ClientR is given by

\[
\{ \} \{ \{ u.i, l.s.p1 \}, \{ u.i, l.s.p2 \}, \{ u.i, l.s.p1, u.i, l.s.p1, u.i, l.s.p2, u.i, l.s.p2 \} \}
\]

Contrary to ClientD, the obligations of ClientR are not singleton sets because ClientR contains potential choices. For instance, in obligation \( \{ u.i, l.s.p1, u.i, l.s.p2 \} \), the traces \( \{ u.i, l.s.p1 \} \) and \( \{ u.i, l.s.p2 \} \) represent potential choices. Note that \( p1 \) and \( p2 \) for the time being are just syntactic suffixes of events. It is first when we make use of event libraries that they come into play.

We assume that basic state machines are autonomous, and therefore executed in parallel. Semantically, parallel composition is defined by interleaving traces. For instance, if the two traces \( (e_1, e_2) \) and \( (e_1', e_2') \) describe the execution of two different independent state machines, then the parallel composition of these traces, written \( (e_1, e_2 \parallel (e_1', e_2')) \), is given by

\[
(\{ e_1, e_2, e_1', e_2' \}, \{ e_1', e_2', e_1, e_2, e_1', e_2 \}, \{ e_1, e_1', e_2, e_1', e_2 \}, \{ e_1', e_1, e_2', e_1, e_2 \})
\]

Each trace describes a possible execution of the two state machines that the traces \( (e_1, e_2) \) and \( (e_1', e_2') \) belong to. Because the state machines are independent and autonomous, all interleavings of the two traces are possible executions. Basic state machines are usually not completely independent because a state machine that expects an input message from another state machine cannot proceed with execution before the message is received. Hence the execution of one state machine may be influenced by another. Traces that describe communication in which messages are sent before they are received are called well formed. We require that all traces in the semantics of a composite state machine to be well formed.
Figure 6: Specification of the server

The semantics of the PM system is the parallel composition of ClientD, Server (as specified in Fig. 6), and ClientR. A fragment of this semantics is shown below

\[
\{\{\}, \{\{?u.i, ls.p1, ?cd.p1, ldb.s, ?db.o\}\}, \\
\{\{?u.i, ls.p1, ?u.i, ls.p2, ?cd.p1, ldb.s, ldb.o, ?cr.p2, ldb.s, ?db.o\}\}\}
\]

Note for instance that the trace \(?u.i, ls.p1, ?cr.p2, ldb.s, ?db.o\) is not well formed because the input event \(?edr.p2\) is not preceded by a corresponding output event in the trace. Note also that the behavior of the user and the data base are not shown. We assume that the PM system is composed with a user and data base that generate all possible inputs to the PM system.

6 Step IV: Verify security adherence

In step IV, the user verifies that the specification adheres to the selected security property. Our method describes what it means for a specification to adhere to a security property, but leaves the choice of evaluation process to the user. The evaluation may be formal, e.g., by theorem proving or by model checking if feasible, or it may be by other means like testing if the specifications are too large for full verification. In the following, we argue only informally that our system specification adheres to the \(N_{\Phi}\) property.

In the definition of the \(N_{\Phi}\) property given in Sect. 4 (see (3)), we considered a system given as a trace set. Since we interpret specifications as sets of obligations, the definition needs to be revisited to take this into account. Instead of requiring that there is a trace \(u\) which prevents the low level user from deducing that confidential behavior has occurred, we require that there is an obligation \(\phi\) such that all its traces prevent the low level user from deducing that confidential behavior has occurred. This means that the obligations, as opposed to the individual traces, provide the unpredictability required by the security property. This is in line with the intuition behind the use of obligations. Formally, we have

\[
N_{\Phi}(\{P\}) \equiv \forall t \in \overline{P} : \exists \phi \in \{P\} : \forall u \in \phi : \neg C(u) \land u \sim_t \theta
\]  
(4)
Note that $\tilde{\Omega}$, where $\Omega$ is a set of obligations, collapses $\Omega$ into a set of traces.

Again, this definition of the $\text{Nr}$ property may be expressed as an instance of a security property schema which is defined for specifications that are interpreted as sets of obligations [33].

The PM system is secure w.r.t. the $\text{Nr}$ property because the developer can never be sure that the researcher has done something regardless of the low level observations he can make. There are two reasons for this. First it is always possible for the researchers to do nothing. Second, the developers cannot influence the behavior of the researcher and vice versa because neither the developers nor the researchers receive input from the server.

To see this, note that the semantics of $\text{ClientR}$ includes the obligation $\{()\}$. By composing this obligation in parallel with the obligations of $\text{ClientD}$ (which describe the observations that the developers can make), and $\text{Server}$, we obtain obligations whose traces do not contain any high level events, i.e., message transmission to or from $\text{ClientR}$. Therefore, for every observation that the developer can make, he can never be sure that the researchers have done something.

7 Step V: Refine system components

In step V, the user refines the specification by removing alternative design choices until all choices are resolved. The security property selected in step II (defined by (Eq. 4)) is preserved under refinement because it is defined in a semantic model which distinguishes between potential choices and inherent choices that should be provided by a system.

Recall that in our running example, it was not initially known whether or not the researcher would use the same communication protocol as the developers for communicating with the server. Assume that it is decided that the researcher clients will use the same protocol as the developer clients. We then refine the specifications of the previous section by removing all the choice alternatives that involve protocol 2. Fig. 7 shows the resulting specification for the server. The same process has to be carried out for the researcher client.

We now make precise what is meant by refinement. Intuitively, obligations
are seen as providing alternative choices which must be provided by an implementation, whereas each trace in an obligation provides a design choice that may be removed under refinement. Formally, a state machine $Q$ is a refinement of a state machine $P$, written $P \rightarrow Q$, iff

$$\forall \phi \in [P]: \exists \phi' \in [Q]: \phi' \subseteq \phi \land (\forall \phi' \in [Q]: \exists \phi \in [P]: \phi' \subseteq \phi)$$  (5)

The reason why NF property is preserved under this notion of refinement is that it is defined (see (4)) such that obligations (as opposed to traces which are used in most standard definitions) may be seen as providing the unpredictability required by the security property. Since choices provided by an obligation cannot be removed under refinement to an implementable specification, this means that the required unpredictability is preserved. Note that a specification whose semantics includes an empty set is not considered implementable. These kinds of specifications may therefore safely be ignored.

In [33] we show that any security property expressed in the security property schema is preserved under refinement.

8 Step VI: Event transformation

In this step VI, the specification (call it abstract) obtained in step V is transformed into a concrete one by replacing the events of the abstract specification by the specification of these events in the event library. At this stage, all design choices are assumed to be decided. In the following we therefore interpret a specification in the standard way as a trace set (see App. A for a formal definition).

An event library is a set of event specifications. An event specification consists of an event definition and a state machine pattern defining the behavior of the event. An event definition is a pair consisting of a kind (1 or ?) and a signal pattern of the form $sf(f_{p1}, \ldots, f_{pn})$ where $f_{p1}, \ldots, f_{pn}$ are formal parameters. A state machine pattern is a state machine that may contain signal patterns.

In the running example, it is assumed that the event specifications for P1 (which takes care of message transmission) are already specified in the library. The specifications of these events are shown in Fig. 8. The figure shows two specifications, one for the receive event (the signal name which is marked by ?) and one for the send event (the signal name which is marked by !). Both specifications have one formal parameter called $mag$.

The P1 protocol works as follows. After a message has been transmitted, the sender starts a timer, and waits for an acknowledgement (i.e., an ack message) from the receiver. If an acknowledgement is not received within a certain time frame, then a timeout is triggered (the full details of this is not shown), and the sender retransmits the message. If an acknowledgement has not been received after the message has been transmitted 10 times, then the sender gives up. Note that the label $to$ in the output event specification (the topest specification of Fig. 8) and the label $from$ (the lowermost specification of Fig. 8) are parameters that are bound to the transmitter and the receiver of the event being replaced, respectively. For instance, Fig. 9 shows the result of replacing the events containing P1 in the upper most specification of Fig. 5 by the event specification of Fig. 8. Here we see that the $to$ parameter has been bound to Server which is the recipient of the message.
We let $T_{EL}$ be the function that takes a basic state machine $P$ and yields the state machine $T_{EL}(P)$ obtained from $P$ by replacing the events occurring in each action expression of $P$ by their definition in event library $EL$. For instance, if $EL$ is the event library of Fig 8, and ClientD is the topmost specification of Fig. 5, then $T_{EL}$(ClientD) yields the specification shown in Fig. 9.

To ensure that transformations preserve semantic equality, i.e., that two abstract specifications that are (semantically) equal are not transformed into two (semantically) different concrete specifications, we require all variables in event specifications to be local. To ensure this, we let $T_{EL}$ rename variables of the event specification such that no name clashes occur when events are being replaced by their definitions. For instance, if the topmost specification of Fig. 8 is applied in a context where the variable $c$ is used, then this variable is given a fresh name which is not used in the context.

In App. B, we show that any event transformation $T_{EL}$ preserve semantic equality, i.e.,

$$[P] = [Q] \Rightarrow [T_{EL}(P)] = [T_{EL}(Q)]$$

It is also shown that an event transformation is entirely characterized by its behavior on events. Technically, this means that the transformation is homomorphic w.r.t. the union and concatenation of trace sets.

The transformation $T_{EL}$ is defined for basic state machines. The transformation induced by an event library $EL$ which takes a composite state machine $P$ as input, is simply the function that applies $T_{EL}$ to all basic state machines that $P$ consists of.

The transformation of a composite state machine may produce concrete traces that have no abstract equivalent due to the granularity we get when events are substituted by state machines. We say that the image of a transformation $T_{EL}$ written $Im_{EL}$, is the set of all concrete traces that have an abstract equivalent. In App. B.2, we show that any event transformation $T_{EL}^C$ for composite state machines preserve semantic equality when restricting attention to the image, i.e.,

$$[P] = [Q] \Rightarrow (\{T_{EL}^C(P)\} \cap Im_{EL}) = (\{T_{EL}^C(Q)\} \cap Im_{EL})$$

It is also shown that the transformation $T_{EL}^C$ for composite state machines is homomorphic w.r.t. union of trace sets when restricting attention to the image.

9 Step VII: Verify adherence preservation

In step VII, the user verifies that the transformation performed in step VI is adherence preserving. Again, we do not consider any particular method of evaluation (e.g., model checking), instead we present the conditions that need to be evaluated.

We say that a transformation preserves a security property $SecP$ if the image of every abstract state machine $P$ that is secure w.r.t. $SecP$ is secure w.r.t. $SecP$, i.e.,

$$SecP([P]) \Rightarrow SecP([T_{EL}(P)] \cap Im_{EL})$$  \hspace{1cm} (6)
We restrict attention to image since we cannot exploit the fact that the abstract specification is secure to ensure that concrete traces that do not have any abstract equivalent do not violate security. This means that additional security analysis may be needed at the concrete level to ensure that those traces that do not have any abstract equivalent do not violate security. However, it should not be necessary to recheck the adherence relationship already established at the more abstract level.

In App. B.3, we define a general condition under which an event transformation preserves a given security property. To obtain the specific condition under which the event library of step VII preserves the NP property, the general condition must be instantiated by the definition of the NP property. We then obtain the following condition:

(a) if an abstract trace contains no high level events, then no concrete traces it is transformed to can contain any high level events;

(b) if two abstract traces are low level equivalent, then no the concrete traces they are transformed to must also be low level equivalent.

Condition (a) is satisfied because the set of high level events is assumed to be all events that can occur in ClientR, and because event transformations cannot change the transmitters or receivers of events.
To check whether condition (b) is satisfied, we may check whether the parallel composition of all corresponding state machine patterns of event specifications in EL (i.e., one send and one receive event) yields the same trace set regardless of context. In the current example, the event library of Fig. 8 satisfies this condition. For instance, the parallel composition of the two event specifications in Fig. 8 when substituted for event $s.d.1$ (in ClientD of Fig. 5) and $?cd.p1$ (in Server of Fig. 6), is

$$\{\{s.sd, ?cd.sd, !cd.a, ?s.a\}, \{s.sd, !s.sd, ?cd.sd, !cd.a, ?s.a\}, \ldots\}$$

Since none of the traces in this set contains a context dependent input event, all traces are well formed regardless of the context they appear in.

10 Related work

This report is related to previous work by the authors [33, 34]. In particular, the security property schema (referred to in Sect. 4) was introduced in [34, 33]. There it was also shown that all security properties of the schema are preserved under refinement. The main difference between this report and [33, 34], is that [33] does not consider transformations at all, and [34] is mostly semantics based; specifications are treated as trace sets, or sets of trace sets. Thus state machines are not considered at all, and a rigorous characterization of syntactic transformations and their interpretation is not given. The main contribution of this report is the definition of UML inspired state machines (Sect. 5 and App. A) and the definition of event transformations (Sect. 8 and App. B).

The state machine notation we use is inspired by UML. Several approaches have given UML state machines a formal semantics (see e.g., [1, 20, 29]). However, none of the works we are aware of distinguishes between explicit and potential nondeterminism.

The semantics of state machines that we propose is based on STAIRS [12], and our notion of refinement corresponds to so-called limited refinement in STAIRS. The main difference between our work and STAIRS is that STAIRS gives a semantics for UML sequence diagrams, whereas we consider state machines. Another difference is that the STAIRS denotational semantics with data [32] describes traces that will never be produced during execution because the values of the data states are allowed to change at any time between assignments. In our denotational semantics, we do not allow this. In this sense, our denotational semantics is more similar to the operational semantics of STAIRS [24].

Our work is related to two somewhat distinct areas of research: model-driven security (MDS) and information flow security. Already published approaches to MDS include [2, 3, 5, 8, 10, 14, 22, 37]. Although interesting, they differ from our work in that they are in most cases of a rather informal nature; the semantics of the languages used (at abstract or and concrete levels) are not sufficiently precise to allow for rigorous reasoning at more than one level of abstraction. Moreover, some of the approaches ([2, 3, 8, 22]) consider transformation of security requirements only, and not transformation of system specifications. These approaches allow adherence checking only at the lowest level of abstraction. Furthermore, others ([5, 8, 10, 37]) do not clearly characterize what it means for
11 Conclusions and future work

We have presented a method for model-driven information flow security. The method has 7 main steps that accommodate the integration of security into design artifacts above the code level. In particular, the method supports (1) specification of systems at different levels of abstraction; (2) rigorous adherence verification at different levels of abstraction; (3) adherence preserving refinement and transformation.

The method is based on UML inspired state machines that we have extended with a construct for specifying potential choice. These state machines, as well as event transformations from abstract to concrete state machines have been given formal syntax and semantics. We have shown that the event transformations satisfy properties that simplify the conditions under which adherence is preserved.

This report is related to previous work of the authors [34, 33]. However, state machines were not considered in [34, 33], and a formal definition of syntactic transformations and their interpretation was not given. The main contribution of this report is the definition of UML inspired state machines (Sect. 8 and App. A) and the definition of event transformations (Sect. 8 and App. B).

Many approaches to model-driven security exist, but all of them are of a rather informal nature. This makes it difficult to precisely define the meaning of adherence or preservation of adherence under refinement and transformation.

We are not aware of any work that address the preservation of information flow security properties under a syntactic notion of transformation.

In future work, we would like to develop syntactic type checking rules which can be used by a tool to automatically check preservation of adherence under transformations.
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References


REFERENCES


A State machines

In this section, we present the syntax and the semantics of our UML inspired state machines.
A.1 Syntax

A state machine is either basic in the sense that it does not consist of other state machines or composite in the sense that it does consist of other state machines. We first present the syntax of basic state machines, then we define the syntax of composite state machines.

A.1.1 Basic state machines

In this section, we first present the graphical syntax of basic UML inspired state machines. Then we define a textual syntax.

We make use of the following syntactic categories:

\[
\begin{align*}
ax & \in AExp \quad \text{arithmetic expressions} \\
bx & \in BExp \quad \text{boolean expressions} \\
sx & \in SExp \quad \text{string expressions}
\end{align*}
\]

Here \(ax\), \(bx\), and \(sx\) are syntactic variables of the three syntactic categories, respectively.

We let \(\text{Exp}\) denote the set of all arithmetic, boolean, and string expressions, and we let \(\text{Var}\) range over this set. Moreover, we assume that the following sets are given:

\[
\begin{align*}
x, y & \in \text{Var} \quad \text{variables} \\
n & \in \text{Num} \quad \text{numerals} \\
st & \in \text{Str} \quad \text{strings}
\end{align*}
\]

We let \(\text{Val}\) denote the set of all values, i.e., numerals, strings, and booleans (\(t\) or \(f\)).

As illustrated in Fig. 10, the constructs which are used for specifying state machines are initial state, simple state, accepting state, transition, action expression, and potential choice.

The state machine illustrated in Fig. 10 specifies a button that whenever pressed, displays the number of times it has been pressed to a user. The number is displayed by sending the signal \(\text{display}(\text{num})\) or \(\text{displayHex}(\text{num})\) (where \(\text{num}\) represents the number of times the button has been pressed) to the user. It is intended that \(\text{display}(\text{num})\) is used for displaying the number in base 10, and that \(\text{displayHex}(\text{num})\) is used for displaying the number in base 16.
(hexadecimal). Whether the button sends the signal \( \text{display}(\text{num}) \) or the signal \( \text{displayHex}(\text{num}) \) should be interpreted as a design choice as specified by the potential choice construct (alt) attached to the branching transitions.

A state describes a period of time during the life of component. The three kinds of states, initial state, simple state, and accepting state, are graphically represented by a black circle, a box with rounded edges, and a black circle encapsulated by another circle, respectively.

A transition represents a move from one state to another. Transitions are labeled by action expressions of the form

\[
\text{nm.si}[\text{bz}]/\text{ef}
\]

Here the expression \( \text{nm.si} \), where \( \text{nm} \) is a state machine name and \( \text{si} \) is a signal, is called a trigger. The expression \( [\text{bz}] \) where \( \text{bz} \) is a boolean expression is called a guard, and \( \text{ef} \) is called an effect. Intuitively, the action should be understood as follows: when signal \( \text{si} \) is received from a state machine with name \( \text{nm} \) and the boolean expression \( \text{bz} \) evaluates to true, then the effect \( \text{ef} \) is executed. An effect is either an assignment or an output expression of the form \( \text{nm.si} \) representing the transmission of signal \( \text{si} \) to the state machine with name \( \text{nm} \).

An assignment is a pair \((x, \text{ex})\) consisting of a variable \( x \) and an expression \( \text{ex} \). The set of all assignments is defined by

\[
\text{Assign} \equiv \text{Var} \times \text{Exp}
\]

In diagrams, assignments are written \( x = \text{ex} \) instead of \((x, \text{ex})\).

We let \( \text{ActExp} \) denote the set of all action expressions and we let \( \text{actx} \) range over this set. Triggers, guards, and effects are all optional parts of an action expression. We sometimes indicate the absence of these parts by \( e \), or we omit to write them all together. For instance, the expressions \( e[\text{bz}]/\text{ef} \) and \( [\text{bz}]/\text{ef} \) are valid and equal action expressions without any trigger.

We let \( \text{Nm} \) denote the set of all state machine names and we let \( \text{nm} \) range over this set. We represent names by strings, thus we have \( \text{Nm} \subseteq \text{Str} \). A signal is a tuple \((\text{st}, x_1, \ldots, x_n)\) where \( \text{st} \) denotes the signal name, and \( x_1, \ldots, x_n \) are the parameters of the signal. We usually write \( \text{st}(x_1, x_2, \ldots, x_n) \) instead of \((\text{st}, x_1, x_2, \ldots, x_n)\). Formally, the set of all signals is defined

\[
\text{SI} \equiv \text{Str} \times \text{Exp}^*
\]

The potential choice construct (graphically represented by an alt) may be attached to branching transitions in a state machine specification to specify alternative design choices.

The graphical notation used for specifying state machines is essentially a subset of the UML statechart notation. However, there are two differences:

- We allow the sender of an input signal to be specified on the transition labels. UML does not have any particular construct for specifying this.

- The potential choice construct is not part of the UML statechart notation.

In theory, the former difference could have been avoided by letting the name of the sender of an input signal be part of the name of the signal. However, by definition of event transformations, we would then have to specify one event
definition for each possible sender or receiver of a signal. This would be impractical. To see this, consider for instance the example of Sect. 8, where the state machine of Fig. 9 is obtained by replacing all signals whose name is P1 in Fig. 5 according to the event definition of Fig. 8. Since the name of the sender or receiver of signals is not part of the signal name, we only have to specify two event definitions: one for the transmission of P1 and one for the reception of P1. However, if the sender or receiver names had been part of the signal name, then we would have to specify one event definition for each possible sender or receiver. This is clearly not a practical solution.

Note that we do not consider our manner of specifying transmission of signals in state machines a deviation from the UML standard. The reason for this is that the standard does not impose any particular restriction on the kind of effects that can be specified or the language they are specified in.

Having defined the graphical syntax of basic state machines, we now move on to define the textual syntax of basic state machines.

Definition A.1 (Basic state machine expression) The set of all basic state machine expressions P is defined by the following grammar

\[ P ::= \text{act} | P^* | P.P | P + P | P[P] \]

The base cases implies that any action (act) is a basic state machine expression. Any other state machine expression is constructed from the basic ones through the application of the operators for iteration (P*), sequential composition (P.P), standard choice (P + P), and potential choice (P[P]).

To define the semantics more conveniently, we do not represent actions exactly as they are represented in the graphical diagrams. Instead, we define the set of all actions Act by

\[ \text{Act} \equiv \text{IE} \cup \{\epsilon\} \times \text{BExp} \cup \{\epsilon\} \times \text{OE} \cup \{\epsilon\} \times \text{Assign} \cup \{\epsilon\} \]  

(7)

where IE and OE denote the set of all input events and output events, respectively. We require that every action must contain at most one event, i.e.,

\[ (ie_e, oe_e, oe_e, \alpha_e) \in \text{Act} \implies (ie_e = \epsilon \lor oe_e = \epsilon) \]  

(8)

Here we have used ie_e to denote an \( \epsilon \) or an input event, i.e., \( ie_e \in \text{IE} \cup \{\epsilon\} \). The same convention is used for boolean expressions, output events, and assignments.

The sets of all input events and output events are defined by

\[ \text{IE} \equiv \{?\} \times M \quad \text{OE} \equiv \{\} \times M \]

where M denotes the set of all messages. An event of the form (l.m) is an output event representing a transmission of message m, whereas an event of the form (r.m) is an input event representing a reception of m. We let E denote the set of all events, i.e.,

\[ E \equiv \text{IE} \cup \text{OE} \]

Messages are of the form (nm_t, nm_r, si), where nm_t is the name of the transmitter state machine of the message, nm_r is the name of the receiver state machine of the message, and si is the signal of the message. The set of all messages is thus defined by

\[ M \equiv \text{Nm} \times \text{Nm} \times \text{SI} \]
We require that the transmitters of output events and receivers of input events in a basic state machine must have the same name. To make this precise, we define the name of an event by the function \(nm \in E \rightarrow Nm\) as follows:

\[
\begin{align*}
\text{nm}(l, (nm_1, nm_r, s)) & \equiv nm_1 \\
\text{nm}(?, (nm_1, nm_r, s)) & \equiv nm_r.
\end{align*}
\]

We let \(E_{nm}\) denote the set of all events with name \(nm\), i.e.,

\[
E_{nm} = \{ e \in E \mid \text{nm}(e) = nm \}.
\]

We let \(nm \in P \rightarrow Nm \cup \{\bot\}\) be the function that yields the name of a state machine expression. The function is defined such that \(nm(P) = nm\) if all events in the actions of \(P\) are members of \(E_{nm}\) for some name \(nm\). Otherwise \(nm(P) = \bot\).

A basic state machine expression (when we ignore the potential choice) is essentially a regular expression. It is well known that the language of regular expressions is equal to the language defined by a finite state machine [19]. Several procedures exist for deriving a regular expression from a finite state machine (see, e.g., [7, 19, 21]). Although state machine expressions have two kinds of choices (instead of just one as in regular expressions), similar techniques could be used to convert a graphical state machine into a textual state machine expression. Of course, we have to translate action expressions of graphical diagrams to actions of textual state machine expressions, but this is trivial.

As an example, the state machine of Fig. 10 is represented by the following state machine expression:

\[
(\epsilon, \epsilon, \epsilon, \text{num} = 0) . ((\text{User}, \text{Button}, \text{pushButton}), \epsilon, \epsilon, \text{num} = \text{num} + 1) . \\
((\epsilon, \epsilon, \text{Button}, \text{User}, \text{display}((\text{num}))), \epsilon) . ((\epsilon, \epsilon, \text{Button}, \text{User}, \text{displayHex}((\text{num})))^*.
\]

### A.1.2 Composite state machines

A composite state machine is a state machine that consists of one or more (basic) state machines. There are two constructs for specifying composite state machines: connector and basic state machine reference. The connector is used to indicate that the basic state machines it is connected to may communicate with each other. Connectors are graphically drawn as lines. The basic state machine reference construct is a reference to the basic state machine whose name equals the name of the reference.

An example of a composite state machine is shown in Fig. 11. The composite state machine consists of two basic state machines (User and Button) and one connector indicating that User and Button may communicate with each other.
The notation we use for specifying composite state machines is a subset of the UML composite structure notation.

We are now ready to define the textual syntax of composite state machine expressions.

**Definition A.2 (Composite state machine expression)** The set of all syntactically correct composite state machine expressions $\mathbf{P}^C$ is defined by the following syntax

$$P ::= P_1 \parallel \cdots \parallel P_n$$

where $P_1, \ldots, P_n$ are basic state machine expressions with single distinct names i.e., $nm(P_i) \neq \perp$ for all $i \in \{1, \ldots, n\}$, and $nm(P_i) \neq nm(P_j)$ if $i \neq j$ for all $i, j \in \{1, \ldots, n\}$.

### A.2 Semantics of state machines without potential choice

In this section, we define the semantics of state machines that do not contain the potential choice construct.

#### A.2.1 Basic state machines

In this section, we define the semantics of basic state machine expressions. Roughly speaking, the semantics of a state machine is the set of sequences obtained by recording all input and output events produced in each possible execution of the state machine. To obtain only those sequences that can be produced during execution, we need a way of evaluating expressions, and a way of storing the variables that are assigned to values during execution. The following auxiliary functions are needed to define this more precisely. The definitions of these functions are inspired by [23].

We let $\text{var} \in \text{Exp} \rightarrow \mathcal{P}(\text{Var})$ be the function that yields the set of all variables in an expression. An expression $ex \in \text{Exp}$ is closed if $\text{var}(ex) = \emptyset$.

We let $\text{CExp}$ denote the set of closed expressions, defined as:

$$\text{CExp} \triangleq \{ex \in \text{Exp} | \text{var}(ex) = \emptyset\}$$

We assume the existence of a function $\text{eval}(\cdot) \in \text{CExp} \rightarrow \text{Val}$ that evaluates any closed expression to a value. In the obvious way, we lift $\text{eval}$ to signals containing closed expressions as arguments and to events that contain such signals. In addition, we lift $\text{eval}$ to actions as follows

$$\text{eval}((ie, bx, oe, e)) \triangleq (\text{eval}(ie), \text{eval}(bx), \text{eval}(oe), e)$$

$$\text{eval}((ie, bx, oe, (x, ex))) \triangleq (\text{eval}(ie), \text{eval}(bx), \text{eval}(oe), (x, \text{eval}(ex)))$$

Let $\sigma \in \text{Var} \rightarrow \text{Exp}$ be a (total) mapping from variables to expressions. We denote such a mapping $\sigma = \{x_1 \rightarrow ex_1, x_2 \rightarrow ex_2, \ldots\}$ for distinct $x_1, x_2, \ldots \in \text{Var}$ and for $ex_1, ex_2, \ldots \in \text{Exp}$. If $ex_1, ex_2, \ldots \in \text{Val}$ we call it a data state.

We let $\Sigma$ denote the set of all mappings and $\Sigma$ denote the set of all data states.

We let $\sigma[x \mapsto ex]$ be the mapping $\sigma$ except that it maps $x$ to $ex$, i.e.,

$$\{x_1 \mapsto ex_1, x_2 \mapsto ex_2, \ldots\}[x \mapsto ex] \triangleq \{x_1 \mapsto ex_1, \ldots, x_i \mapsto ex_i, \ldots\}$$

where $x = x_i$ for some $i \in \{1, \ldots, n\}$
We generalize $\sigma[x \mapsto ex]$ to $\sigma[\sigma']$ in the following way:

$$\sigma'[\{x_1 \mapsto ex_1, \ldots, x_n \mapsto ex_n\}] \equiv [\sigma[x_1 \mapsto ex_1] \cdots [x_n \mapsto ex_n]$$

We let $\sigma(x)$ denote the expression that $x$ maps to. The mapping is lifted to expressions such that $\sigma(ex)$ yields the expression obtained from $ex$ by simultaneously substituting the variables of $ex$ with the expressions that these variables map to in $\sigma$. We lift $\sigma$ to signals and events in the same way, and to actions as follows

$$\sigma((ie_i, bx_i, oe_i, e)) \equiv (\sigma(ie_i), \sigma(bx_i), \sigma(oe_i), e)$$

$$\sigma((ie_i, bx_i, oe_i, (x, ex))) \equiv (\sigma(ie_i), \sigma(bx_i), \sigma(oe_i), (x, \sigma(ex)))$$

To define the semantics of a basic state machine, i.e., the sequences of events that can be produced during execution, it is necessary to record the change in data state. We define an intermediate semantics for this purpose. The intermediate semantics is defined in terms of sequences of so-called action-state triples. Action-state triples are of the form

$$\text{(act, } \sigma, \sigma')$$

where $\sigma$ denotes the so-called pre-state before act is executed, and $\sigma'$ denotes the post-state after act has been executed, e.g., if act contains an assignment $(x, ex)$, then $\sigma'$ is the state $\sigma$ except that $x$ maps to $ex$.

To define the intermediate semantics, we make use of the concatenation operator $\cdot$. If $s$ and $t$ are sequences, then $s \cdot t$ yields the sequence obtained by concatenating $s$ and $t$. We lift the concatenation operator to sequence sets such that $\Phi \cdot \Phi'$ yields the set obtained by concatenating all sequences of $\Phi$ with all sequences of $\Phi'$. Also, we denote by $\Phi^n$, the set of sequences obtained by concatenating $\Phi$ $n$ times. In particular, $\Phi^0$ is defined by $\{\emptyset\}$. For example, we have that $\Phi^5$ yields $\Phi$ concatenated five times, i.e.,

$$\Phi^5 = \Phi \cdot \Phi \cdot \Phi \cdot \Phi \cdot \Phi$$

We are now ready to define the intermediate semantics of state machines.

**Definition A.3 (Intermediate semantics of basic state machines)** The intermediate semantics of a basic state machine expression $P$, written $\{P\}$, is defined as

$$\{\text{ie}_i, \text{bx}_i, \text{oe}_i, e) \equiv \{\text{eval}(\sigma(\text{ie}_i, \text{bx}_i, \text{oe}_i, e)), \sigma, \sigma) | \sigma \in \Sigma\}$$

$$\{\text{ie}_i, \text{bx}_i, \text{oe}_i, (x, ex)) \equiv \{\text{eval}(\sigma(\text{ie}_i, \text{bx}_i, \text{oe}_i, (x, ex))), \sigma, \sigma[x \mapsto \text{eval}(\sigma(ex))]) \sigma \in \Sigma\}$$

$$\{P \cdot Q\} \equiv \{P\} \cdot \{Q\}$$

$$\{P + Q\} \equiv \{P\} \cup \{Q\}$$

$$\{P^n\} \equiv \bigcup_{i=0}^{n} \{P\}$$

The intermediate semantics is defined in a modular way in the sense that the meaning of an expression is not affected by the context it appears in. This is achieved by letting actions be evaluated under all possible data states. A consequence of this is that the intermediate semantics describes action sequences that will never be produced during execution. There are two reasons for this:
(a) The sequences of the intermediate semantics may contain actions whose guards are evaluated to false. During execution, however, these actions will never occur.

(b) Because the pre-state of a triple is not required to be equal to the post-state of the triple preceding it, values of the data states may implicitly change at any time between assignments, while this would never occur during execution.

If a sequence $s$ of action-state triples does not exhibit problems (a) and (b), we say that it is well formed, and write $wft(s)$ to indicate this. Formally, well formed sequences of action-state triples are inductively defined by the following rules:

\[
\frac{}{wft(\langle \rangle)} \quad \frac{wft(\langle (i, e, s, a, \sigma, \sigma') \rangle)}{wft(s \sim (\langle act, a, \sigma \rangle, \langle act', a', \sigma' \rangle))} \quad \frac{wft(s \sim (\langle act, a, \sigma \rangle, \langle act', a', \sigma' \rangle))}{wft(s \sim (\langle act, a, \sigma, \sigma' \rangle, \langle act', a', \sigma' \rangle))}
\]

We let $A$ denote the set of all well formed action-state triple sequences, i.e.,

\[ A \triangleq \{ s \in (\text{Act} \times \bar{\Sigma} \times \bar{\Sigma})^* \mid wft(s) \} \] (10)

We are only interested in the input-output behavior of state machines. To make this precise we let $io \in (\text{Act} \times \bar{\Sigma} \times \bar{\Sigma})^* \to E^*$ be the function that takes a sequence $s$ of action-state triples and produces the sequence of events obtained from $s$ by removing all data states, assignments, and guards of $s$. The $io$ function is formally defined by the following rules:

\[
\begin{align*}
    io(\langle \rangle) & \triangleq \langle \rangle \\
    io(\langle (i, e, s, a, \sigma, \sigma') \rangle \sim t) & \triangleq \langle (i, e) \sim t \rangle \\
    io(\langle (i, e, s, a, \sigma, \sigma') \rangle \sim t) & \triangleq \langle (i, a) \sim t \rangle \\
    io(\langle (e, bx, e, a, \sigma, \sigma') \rangle \sim t) & \triangleq \langle \epsilon \rangle \\
    io(\langle (e, bx, e, a, \sigma, \sigma') \rangle \sim t) & \triangleq \langle (i, e, bx, e, a, \sigma, \sigma') \rangle \sim t
\end{align*}
\]

(11)

We lift $io$ to sets of action-state triple sequences such that $io(\Phi)$ yields the set of sequences obtained by applying $io$ to each sequence in $\Phi$. For example, we have that

\[ io(\langle \langle ?, m, bx, e, (e, ex) \rangle, \sigma_1, \sigma_1', \langle bx', (l, m'), \epsilon \rangle, \sigma_2, \sigma_2' \rangle) = \langle \langle ?, m, (l, m') \rangle \rangle \]

**Definition A.4 (Semantics of basic state machines)** The semantics of a basic state machine expression $P$, written $[ P ]$, is defined

\[ [ P ] \triangleq io([ P ] \cap A) \]

**A.2.2 Composite state machines**

We assume that basic state machines are autonomous, and therefore executed in parallel. Semantically, parallel composition is defined by *interleaving* traces.

A trace $t$ is an interleaving of the traces $s_1, \ldots, s_n$, written $\text{inl}(s_1, \ldots, s_n, t)$, iff $s_1, \ldots, s_n$ are sub-sequences of $t$, i.e.,

\[
\text{inl}(\langle \langle \rangle, \ldots, \langle \rangle \rangle, \langle \rangle) \quad \frac{iinl(s_1, \ldots, s_n, t)}{\langle \langle s_1, \ldots, s_n, t \rangle \rangle}
\]

\[
\frac{iinl(s_1, \ldots, s_n, \langle \rangle \sim t)}{\langle \langle s_1, \ldots, s_n, e \sim t \rangle \rangle}
\]
We are only interested in interleavings that are well formed in the sense that they describe communication in which messages are sent before they are received. We write \( wft(s) \) if trace \( s \) is well formed. The predicate is formally defined by the rules:

\[
\frac{-}{wft(\langle \rangle)} \quad \frac{wft(s \sim t) \text{ if } e \in \text{OE}}{wft(s \sim (e \sim t))} \quad \frac{wft(s \sim t)}{wft(s \sim ((l, m)) \sim t \sim (\langle ?, m \rangle))}
\]

We let \( T \) denote the set of all well formed traces, i.e.,

\[
T \equiv \{s \in E^* \mid wft(s)\}
\]

We define the parallel composition operator as the set of all well formed interleavings of its arguments, i.e.,

\[
\parallel (s_1, \ldots, s_n) \equiv \{t \in T \mid \text{intl}((s_1, \ldots, s_n), t)\}
\]

The operator is lifted to trace sets as follows

\[
\parallel (\Phi_1, \ldots, \Phi_n) \equiv \bigcup_{s_1 \in \Phi_1, \ldots, s_n \in \Phi_n} (s_1, \ldots, s_n)
\]

Definition A.5 (Semantics of composite state machines) The semantics of a composite state machine expression \( P = (P_1 \parallel \ldots \parallel P_n) \), written \([P]\), is defined by

\[
[P] \equiv \parallel ([P_1], \ldots, [P_n])
\]

A.3 Semantics of state machines with potential choice

The semantics of a state machine expression with potential choice is a set of trace sets called obligations. Intuitively, the traces of an obligation are equivalent in the sense that an implementation is only required to produce one of them. Thus, each trace of an obligation represents underspecification, i.e., a potential choice. However, an implementation is required to produce at least one trace in each obligation. Thus obligations represent explicit choices that have to be present in an implementation.

To define the semantics formally, we lift the concatenation operator \( \cdot \) to sets of obligations as follows

\[
\Omega \cdot \Omega' \equiv \{\phi \cdot \phi' \mid \phi \in \Omega \land \phi' \in \Omega'\}
\]

We denote by \( \Omega^n \), the set of obligations \( \Omega \) concatenated \( n \) times. We define \( \Omega^0 \) by \( \{\{\}\} \).

We define the inner union of two sets of obligations, written \( \Omega \cup \Omega' \), by

\[
\Omega \cup \Omega' \equiv \{\phi \cup \phi' \mid \phi \in \Omega \land \phi' \in \Omega'\}
\]

As we did in the previous section, we first define the intermediate semantics.

Definition A.6 (Intermediate semantics of basic state machines) The intermediate semantics of a basic state machine \( P \) with potential choice, written \([|P|]\), is defined
\[ \langle \text{ie}_e, b x_e, o e_e, c \rangle \]  
\[ \langle \text{ie}_e, b x_e, o e_e, (x, e x) \rangle \]  
\[ \{ \langle \text{eval}(\sigma((\text{ie}_e, b x_e, o e_e, c)), \sigma), \sigma \rangle \mid \sigma \in \Sigma \} \]  
\[ \{ \langle \text{eval}(\sigma((\text{ie}_e, b x_e, o e_e, (x, e x))), \sigma, \sigma(x \mapsto \text{eval}(\sigma(e x)))) \rangle \mid \sigma \in \Sigma \} \]  
\[ \langle P, Q \rangle \]  
\[ \{ P \} \cap \{ Q \} \]  
\[ \{ P \} \cup \{ Q \} \]  
\[ \{ P \} \cup \{ Q \} \cup \{ P \} \]  
\[ \{ \emptyset(P) \} \]  

We are now ready to define the semantics of state machine expressions with potential choices.

**Definition A.7 (Semantics of basic state machines)** The semantics of a basic state machine expression \( P \) with potential choice, written \([ P ]\), is defined by

\[
[ P ] \triangleq \{ (\phi \cap \mathcal{A}) \mid \phi \in \{ P \} \}
\]

The semantics of a composite state machine expression is the parallel composition of the (basic) state machine expressions it consists of.

**Definition A.8 (Semantics of composite state machines)** The semantics of the composite state machine expression \( P = (P_1 \parallel \cdots \parallel P_n) \) with potential choice, written \([ P ]\), is defined

\[
[ P ] \triangleq \bigcup_{\phi_i \in \{ P_1 \} \cdots \phi_n \in \{ P_n \}} \{ \{ \phi_1, \ldots, \phi_n \} \}
\]

**B Event transformations**

In this section, we give a formal characterization of event transformations and show that these transformations satisfy some desirable properties. In Sect. B.1, we consider transformation of basic state machines, whereas in Sect. B.2, transformation of composite state machines is considered.

**B.1 Basic state machines**

In this section, we give a formal characterization of the transformation which is induced by an event library. The transformation is defined for state machines without potential choices.

An event library is a set of event specifications. An event specification is a pair \((e^d, P^p)\) consisting of an event definition \(e^d\) and a state machine pattern \(P^p\) defining the behavior of the event. An event definition is of the form \((k, st^d)\) where \(k\) is a kind \((!\! or \?\?)\), and \(st^d\) is a signal definition of the form \(st(f_{p_1}, \ldots, f_{p_n})\) where \(f_{p_1}, \ldots, f_{p_n}\) are formal parameters. We let \(\text{FP}\) denote the set of all formal parameters. The set of all signal definitions \(\text{SI}^d\) and event definitions \(E^d\) are defined by

\[
\text{SI}^d \triangleq \text{Str} \times \text{FP}^* \quad E^d \triangleq \{!, ?\} \times \text{SI}^d
\]

A state machine pattern \(P^p\) is a state machine expression that may contain formal parameters. To make this more precise, we define the notion of expression pattern, event pattern, and action pattern.
An expression pattern \( \text{Exp} \) is an expression that may contain formal parameters. We let \( \text{Exp} \) denote the set of all expression patterns, and \( \text{BExp} \) denote the set of all boolean expression patterns. Note that \( \text{Exp} \subseteq \text{Exp} \) and \( \text{BExp} \subseteq \text{BExp} \).

An event pattern \( \text{E} \) is an event whose transmitter and receiver may contain special formal parameters and whose signals may contain expression patterns. The set of all expression patterns \( \text{E} \) is defined by

\[
\text{E} = \{!, ?\} \times ((\text{Nm} \cup \{!\}) \times (\text{Nm} \cup \{?\}) \times (\text{FP} \cup (\text{Str} \times (\text{Exp}^*))))
\]

where to and from are special formal parameters, i.e., \( \text{to}, \text{from} \in \text{FP} \).

We let \( \text{IE} \) and \( \text{OE} \) denote the set of all input event patterns and output event patterns, respectively. These sets are defined by

\[
\text{IE} = \{(k, m^p) \in \text{E} \mid k = ?\} \quad \text{OE} = \{(k, m^p) \in \text{E} \mid k = !\}
\]

The set of all action patterns \( \text{Act} \) is defined by

\[
\text{Act} = (\text{IE} \cup \{!\}) \times (\text{BExp} \cup \{!\}) \times (\text{OE} \cup \{!\}) \times ((\text{Var} \times \text{Exp}) \cup \{!\})
\]

We are now ready to define state machine patterns precisely.

**Definition B.1 (State machine pattern)** The set of all state machine patterns \( \text{P} \) is defined by the following grammar

\[
\text{P} ::= \text{act} | (\text{P})^* | \text{P} \cdot \text{P} | \text{P} + \text{P}
\]

where \( \text{act} \in \text{Act} \).

The notion of an event library is made precise by the following definition.

**Definition B.2 (Event library)** The set of all event libraries \( \text{EL} \) is given by

\[
\text{EL} = \text{Ed} \times \text{P}
\]

For every event library \( \text{EL} \) in \( \text{EL} \), we require the signal definitions in \( \text{EL} \) to be unique in the following sense

\[
((k, st(f_1, \ldots, f_j)), P^p_1), ((k, st(f_1, \ldots, f_j), P^p_2)) \in \text{EL} \implies j \neq k
\]

In addition, we require that each event specification \( ((k, (si, P^p)) \) in \( \text{EL} \) must satisfy

\[
\text{ffp}(P^p) \subseteq \text{ffp}(si)
\]

where the function \( \text{ffp} \) yields the set of all formal parameters in a signal definition or a state machine pattern.

When a transformation induced by an event library \( \text{EL} \) is applied to a state machine \( \text{P} \), all events in \( \text{P} \) for which there is a matching event definition in \( \text{EL} \), are replaced by their specification in \( \text{EL} \). An event \( (k, (nm_1, nm_2, si)) \) matches an event definition \( (k, st^p) \) if there is a substitution \( \text{sub} \) that replaces formal parameters by expressions such that \( si = \text{sub}(st^p) \). More precisely, a substitution \( \text{sub} \in \text{FP} \rightarrow \text{Exp} \) is a function that replaces formal parameters by expressions. We let \( \text{Sub} \) denote the set of all substitutions. Any substitution
sub is lifted to expression patterns such that sub(ex
p
) yields the expression obtained from ex
p
 by replacing each formal parameter fp in ex
p
 by sub(fp).
If ex
p
 contains no formal parameters, then sub(ex
p
) = ex
p
. Substitutions are further lifted to event patterns and assignment patterns as follows

\[
\begin{align*}
\text{sub}(k, \langle m_{n1}^p, m_{n2}^p, s_i(ex_1^p, \ldots, ex_n^p) \rangle) & \equiv (k, \langle \text{sub}(m_{n1}^p), \text{sub}(m_{n2}^p), s_i(\text{sub}(ex_1^p), \ldots, \text{sub}(ex_n^p)) \rangle) \\
\text{sub}(\langle x, \text{ex}^p \rangle) & \equiv (x, \text{sub}(\text{ex}^p)) \\
\text{sub}(\langle \text{ie}_p, \text{bx}_p, \text{oe}_p, a_p \rangle) & \equiv (\text{ie}_p, \text{bx}_p, \text{oe}_p, a_p) \\
\text{sub}(P_{1}^p, P_{2}^p) & \equiv \text{sub}(P_{1}^p) \text{sub}(P_{2}^p) \\
\text{sub}(P_1^p + P_2^p) & \equiv \text{sub}(P_1^p) + \text{sub}(P_2^p) \\
\text{sub}(P^*) & \equiv \text{sub}(P^*)
\end{align*}
\]

and to state machine patterns as follows

\[
\begin{align*}
\text{sub}(k, \langle \text{ie}_p^e, \text{bx}_p^e, \text{oe}_p^e, a_p^e \rangle) & \equiv (k, \langle \text{sub}(\text{ie}_p^e), \text{sub}(\text{bx}_p^e), \text{sub}(\text{oe}_p^e), \text{sub}(a_p^e) \rangle) \\
\text{sub}(P_{1}^e, P_{2}^e) & \equiv \text{sub}(P_{1}^e) \text{sub}(P_{2}^e) \\
\text{sub}(P_1^e + P_2^e) & \equiv \text{sub}(P_1^e) + \text{sub}(P_2^e) \\
\text{sub}(P^*^e) & \equiv \text{sub}(P^*^e)
\end{align*}
\]

Note that \text{ie}_p^e denotes an input event pattern or an \varepsilon, i.e., bx_p^e \in \text{IE}_p^e \cup \{\epsilon\}.

The same convention is used also for boolean expression patterns, output event patterns, and assignment patterns.

We let \text{te}_E(.) \in \text{EL} \rightarrow (E \rightarrow P) be the function that replaces events by their definition in an event library. Formally, the function is defined by

\[
\text{te}_E((k, \langle m_{n1}, m_{n2}, s_i \rangle)) \equiv \text{sub}(\langle \text{to} \rightarrow m_{n1}, \text{from} \rightarrow m_{n2} \rangle)(P^e) \\
\text{if there are } ((k, s_i^p), P^p) \in \text{EL} \\
\text{and } s_i \in \text{Sub} \text{ such that } \text{sub}(s_i^p) = s_i
\]

Note that the special formal parameters \text{to} and \text{from} are bound to the transmitter and the receiver of messages, respectively. As we did in App. A.2, we let \text{sub}[\text{fp} \rightarrow \text{ex}] denote the mapping \text{sub} except that \text{fp} maps to \text{ex}.

The function \text{tact}(.) \in \text{EL} \rightarrow (\text{Act} \rightarrow P) that substitutes events in actions by their specification in an event library is defined as follows

\[
\begin{align*}
\text{tact}_E((\langle \text{ie}_e, \text{bx}_e, \epsilon, a_e \rangle)) & \equiv (\epsilon, \text{bx}_e, \epsilon, a_e), \text{tact}_E((\langle \text{ie}_e, \epsilon, \epsilon, a_e \rangle) \\
\text{if } \text{ie} \in \text{Dom}(\text{te}_E) \\
\text{tact}_E((\langle \text{ie}_e, \text{bx}_e, \epsilon, a_e \rangle)) & \equiv (\epsilon, \text{bx}_e, \epsilon, a_e), \text{tact}_E((\langle \epsilon, \epsilon, a_e \rangle) \\
\text{if } \epsilon \in \text{Dom}(\text{te}_E) \\
\text{tact}_E((\langle \text{ie}_e, \text{bx}_e, \epsilon, a_e \rangle)) & \equiv (\text{ie}_e, \text{bx}_e, a_e, a_i) \\
\text{if } \epsilon \notin \text{Dom}(\text{te}_E) \text{ and } a_i \notin \text{Dom}(\text{te}_E)
\end{align*}
\]

We let \text{TE}_L be the function that takes a basic state machine \text{P} and yields the state machine \text{TE}_L(\text{P}) obtained from \text{P} by replacing the events occurring in each action of \text{P} by their definition in \text{EL}. We let \text{TE}_L rename variables of the event specification such that no name clashes occur when events are being replaced by their definitions. To make this precise, we make use of a renaming function \text{rnm}(. \rightarrow .) \in P \times EL \rightarrow EL. That is, \text{rnm}(P, EL) yields the event library obtained from \text{EL} by renaming all its variables such that the following condition is satisfied

\[
\text{var}(\text{rnm}(P, EL)) \cap \text{var}(P) = \emptyset
\]
where the function var is lifted to event libraries and state machines such that var(EL') yields the set of all variables occurring in the state machine patterns of EL', and sub(P) yields the set of all variables occurring in P.

The event transformation T_{EL} that applies tact_{EL} to all actions of a basic state machine is given by the following definition.

**Definition B.3 (Basic event transformation)** The event transformation $T_{EL}(\cdot) \in EL \rightarrow (P \rightarrow P)$ for basic state machines is defined by

$$T_{EL}(P) \equiv T'_{renm(P,EL)}(P)$$

where $T'_{renm}(\cdot) \in EL \rightarrow (P \rightarrow P)$ is defined by

- $T'_{EL}(\text{act}) \equiv \text{tact}_{EL}(\text{act})$
- $T'_{EL}(P,Q) \equiv T'_{EL}(P,T'_{EL}(Q))$
- $T'_{EL}(P + Q) \equiv T'_{EL}(P) + T'_{EL}(Q)$
- $T'_{EL}(P^*) \equiv T'_{EL}(P)^*$

**Definition B.4 (Semantics of basic event transformations)** The semantics of a transformation $T_{EL}$, written $\llbracket T_{EL} \rrbracket$, is defined by

$$\{ \{ P \}, \{ T_{EL}(P) \} \} \mid P \in \text{Dom}(T_{EL})$$

Here $\text{Dom}(T_{EL})$ yields the domain of the function $T_{EL}$.

The renaming of variables in event libraries ensures that event specifications are side effect free. This has the consequence that event transformations preserve semantic equality and are therefore functional.

**Theorem B.1** The relation $[T_{EL}]$ is a function.

The following lemmas tell us that the semantics of an event transformation is entirely characterized by its behavior on events. This property is useful when defining conditions under which event transformations are security preserving.

**Lemma B.1** If EL is an event library, and P and Q be basic state machines whose variables are disjoint from those in EL, then $[T'_{EL}]$ is homomorphic w.r.t. union, i.e.,

$$[T'_{EL}([P] \cup [Q])] = [T'_{EL}([P])] \cup [T'_{EL}([Q])]$$

**Lemma B.2** If EL is an event library, and P and Q be basic state machines whose variables are disjoint from those in EL, then $[T'_{EL}]$ is homomorphic w.r.t. concatenation, i.e.,

$$[T'_{EL}([P] \cdot [Q])] = [T'_{EL}([P])] \cdot [T'_{EL}([Q])]$$

**B.2 Composite state machines**

The transformation induced by an event library EL which takes a composite state machine P as input, written $T^C_{EL}$, is simply the function that applies $T_{EL}$ to all basic state machines that P consists of.
Definition B.5 (Composite event transformations) The composite transformation $T_{EL}^{C}(\cdot) \in EL \rightarrow \langle P^C \rightarrow P^C \rangle$ induced by an event library is defined

$$T_{EL}^{C}(P_1 \parallel \cdots \parallel P_n) \triangleq T_{EL}^{C}(P_1) \parallel \cdots \parallel T_{EL}^{C}(P_n)$$

where $EL' = \text{rm}(P_1 \parallel \cdots \parallel P_n, EL)$.

The semantics of $T_{EL}^{C}$ is defined exactly in the same way as for $T_{EL}$.

Definition B.6 (Semantics of composite event transformations) The semantics of the transformation $T_{EL}^{C}$, written $[T_{EL}^{C}]$, is the relation defined by

$$\{(\llbracket P \rrbracket, \llbracket T_{EL}^{C}(P) \rrbracket) \mid P \in \text{Dom}(T_{EL}^{C})\}$$

A transformation $T_{EL}^{C}$ may in general transform non well formed traces resulting from parallel composition at the abstract level into well formed traces at the concrete level. We say that the image of a transformation $T_{EL}^{C}$, written $\text{Im}_{EL}$, is the set of all concrete traces that have an abstract equivalent, i.e., the set of all concrete traces that correspond to well formed abstract traces. Formally, $\text{Im}_{EL}$ is the least set satisfying the following condition

$$\forall i \in \{1, \ldots, n\} \ (s_i \in (E_{n0,i})^* \land s_i' \in \llbracket T_{EL}^{C}(\{s_i\}) \rrbracket \land (s_1', \ldots, s_n') \neq \emptyset \implies (s_1', \ldots, s_n') \subseteq \text{Im}_{EL}$$

(16)

for all $\{nm_1, \ldots, nm_n\} \subseteq Nm$.

The semantics of transformation $T_{EL}^{C}$ restricted to its image, written $\hat{T}_{EL}$, is defined

$$\hat{T}_{EL} \triangleq \{(\llbracket P \rrbracket, \llbracket T_{EL}^{C}(P) \cap \text{Im}_{EL} \rrbracket) \mid P \in \text{Dom}(T_{EL}^{C})\}$$

(17)

Theorem B.2 The relation $[T_{EL}^{C}]$ is a function when restricted to its image, i.e.,

$$[P] = [Q] \implies [T_{EL}^{C}(P) \cap \text{Im}_{EL}] = [T_{EL}^{C}(Q) \cap \text{Im}_{EL}]$$

Lemma B.3 The semantics of the event transformation for composite state machines induced by an event library $EL$ is homomorphic w.r.t. the union operator on trace sets when restricted to its image, i.e.,

$$\hat{T}_{EL}(\llbracket P \rrbracket \cup \llbracket Q \rrbracket) = \hat{T}_{EL}(\llbracket P \rrbracket) \cup \hat{T}_{EL}(\llbracket Q \rrbracket)$$

B.3 Adherence preservation under event transformations

In this section, we present general conditions under which security properties are preserved under event transformations. We then show how these general conditions can be instantiated into specific conditions for particular security properties.

We recall from [34, 33], that a security property $SecP$ is a conjunction of basic security predicates $\text{BSP}_{\text{rev}}(\Phi)$ of the form

$$\forall s, t \in \Phi : s \xrightarrow{c} t \implies \exists u \in \Phi : s \xrightarrow{h} u \xrightarrow{c} t$$

(18)

for the restriction relation $c$, the high level relation $h$, and the low-level equivalence relation $l$ on traces (i.e., $c, h, l \subseteq E^* \times E^*$). Note that if $c$ is a relation, we write $s \xrightarrow{c} t$ for $(s, t) \in c$, and $s \xrightarrow{c} t$ for $(s, t) \in c$ if $c$ is an equivalence relation.
We will henceforth use the following definition of the low level equivalence relation $\sim_l$

$$s \sim_l t \iff s|_L = t|_L \quad (19)$$

for a set of low level events $L$.

A transformation is secure w.r.t. a security property $SecP$ if the image of an abstract specification $P$ is secure w.r.t. $SecP$ if $P$ is secure w.r.t. $SecP$. We restrict attention to the image since we cannot exploit the fact that the abstract specification is secure to ensure that concrete traces that do not have any abstract equivalent do not violate security. This means that additional security analysis is needed at the concrete level to ensure that those traces that do not have any abstract equivalent do not violate security.

**Definition B.7 (Security preservation)** Let $EL$ be an event library and $SecP$ be a security property. Then transformation $T^C_{EL}$ induced by $EL$ preserves security property $SecP$ for specification $P$ iff

$$SecP([P]) \implies SecP(T^C_{EL}([P]))$$

The following theorem presents general conditions under which a basic security predicate is preserved by a transformation.

**Theorem B.3** Let $\tau$ be a restriction, $h$ be a high level relation, and $T^C_{EL}$ be the transformation induced by event library $EL$. Then $T^C_{EL}$ preserves $BSP_{\tau h}$ for specification $P$ if the following conditions are satisfied for all $s, t, u \in [P]$, $s' \in T^C_{EL}(\{s\}), t' \in T^C_{EL}(\{t\})$

$$s' \sim \tau t' \implies s \sim \tau t \quad (20)$$

$$s \sim \tau t \land s \overset{h}{\rightarrow} u \sim_l t \implies \exists u' \in T^C_{EL}(\{u\}) : s' \overset{h}{\rightarrow} u' \sim_l t' \quad (21)$$

Note that we have exploited Lemma B.3 in the definition of the conditions since the property ensures that the transformation $T^C_{EL}$ is defined for singleton sets.

Obviously, a transformation preserves a security property $SecP$ if the conditions are satisfied for all basic security predicates that $SecP$ consists of.

**Corollary B.1** Let $SecP$ be a security property, i.e., a conjunction of basic security predicates. Then $SecP$ is preserved under a transformation $T^C_{EL}$ if each basic security predicate of $SecP$ satisfies the conditions (20) and (21) of Theorem B.3.

The conditions of Theorem B.3 may be used to derive specific conditions that can be used to check that a transformation preserves a given security property. The procedure for deriving such conditions for an arbitrary security property $SecP$ and event library $EL$ is as follows. For each basic security predicate $BSP_{\tau h}$ that $SecP$ consists of

- define two conditions $C_l$ and $C'_l$ and show that $C_l$ implies (20) and $C'_l$ implies (21) for the transformation $T^C_{EL}$ of basic state machines induced by $EL$
• define two conditions $C_2$ and $C'_2$ and show that $C_1 \land C_2$ implies (20) and $C'_2 \land C'_2$ implies (21) for the composite transformation $T_{EL}^{\overline{L}}$ induced by $EL$.

In the following we illustrate the procedure for the non-inference property [36].

**Definition B.8** The security property non-inference, written NP, consists of the basic security predicate $BS_{E2}$ whose restriction $t$ and high level relation $\overline{t}$ are defined

\[
\begin{align*}
    s \overset{a}{\rightarrow} t & \iff \text{true} \\
    s \overset{b}{\rightarrow} t & \iff t_H = \emptyset
\end{align*}
\]

According to the procedure, we must first define two conditions $C_1$ and $C'_1$ and show that $C_1$ implies (20) and $C'_1$ implies (21), respectively. By definition of $t$, any transformation will trivially satisfy condition (20). Thus we need only consider the latter condition (21). We first instantiate (21) for the basic security predicate defining the NP property. We get for all traces $t$ and $u$, and all $t' \in T_{EL}(\{t\})$

\[
u_H = \emptyset \land u|_L = t|_L \implies \exists u' \in T_{EL}(\{u\}) : u'|_H = \emptyset \land u'|_L = t'|_L
\]  

(22)

To define a condition $C'_1$ that implies (22) for basic transformations, we first examine the case in which $[T_{EL}]$ is applied to single events. We can do so because $[T_{EL}]$ is homomorphic w.r.t. union and concatenation of trace sets (by Lemma B.1 and Lemma B.2). It is easy to see that any transformation that transforms a non high level event into a set of traces that contain a high level event will fail to satisfy condition (22). Hence, we define a condition that ensures that this never occurs:

\[
    e \notin H \implies \forall s \in [T_{EL}](\{\{e\}\}) : s_H = \emptyset
\]  

(23)

The same consideration holds for low level events; if a non low level event is transformed into a set of traces that contain a low level event, then condition (22) is broken. Therefore we must require

\[
    e \notin L \implies \forall s \in [T_{EL}](\{\{e\}\}) : s|_L = \emptyset
\]  

(24)

When the set $L$ of low level events, and the set $H$ of high level events are defined as all events that may occur in particular state machines, then conditions (23) and (24) are trivially satisfied because event transformations cannot change the transmitters or receivers of events. By exploiting Lemma B.2, it follows by induction over the length of traces $u$ and $t$ that any transformation on basic state machines satisfying conditions (23) and (24) will satisfy the condition (22).

The final step of the procedure is to define a condition $C'_3$ and to show that this condition together with (23) and (24) implies (22) for transformations of composite state machines. To obtain $C'_3$ we observe that the first part of condition (22) $(u'|_H = \emptyset)$ is ensured by (23). However, this is not the case for the second part $(u'|_L = t'|_L)$ which requires low level equality to be preserved. This condition can be rephrased as

\[
    \overline{T_{EL}(\{t|_L\})} = \overline{T_{EL}(\{t\})}_L
\]  

(25)

for all traces $t$. 

---

**B. EVENT TRANSFORMATIONS**  

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The reason why condition (23) may not be satisfied is that low level observations on the abstract level may be transformed into several concrete low level observations that may be part of traces that are not well formed. To see this, consider the upper most composite state machine of Fig. 12. Let the set of low level events \( L \) be the set of events that occur in state machine \( P_1 \), and the set \( H \) of high level events be \{?d, ?f\}. The semantics of \( A \) is
\[
[A] = \{ \langle la \rangle, \{ ?a, ?a, ?c \}, \{ ?d \} \} = \{ \langle la \rangle, \{ ?a, ?a, ?c \}, \{ ?d \} \}
\]
The specification is secure w.r.t. the \( \text{NF} \)-property because when the low level user observes \( \langle la \rangle \) (this is the only observation the low level user can make), he will not know whether the high level event \( ?d \) will occur.

Now let \( EL \) be an event library defined such that \( T_{EL}(k) = C \) where \( C \) is the lower most specification of Fig. 12. Specification \( C \) has the following semantics
\[
[C] = \{ \langle la_1, la_1 \rangle, \{ ?a_1, ?a_1 \}, \langle la_2, la_2 \rangle, \{ ?a_1, ?a_1 \}, \{ ?f \} \} = \{ \langle la_1, la_1 \rangle, \{ ?a_1, ?a_1 \}, \{ ?f \} \}
\]
The specification \( C \) is not secure w.r.t. the \( \text{NF} \) property because the low level user will know that the high level event \( ?f \) occurs when the observation \( \langle la_2, la_2 \rangle \) is made. The problem is that the low level observation \( la_1 \) has been transformed into the two observations \( \langle la_1, la_1 \rangle \) and \( \langle la_2, la_2 \rangle \). But the trace in which both observation \( \langle la_2, la_2 \rangle \) and event \( ?f \) (instead of the high level event \( ?f \)) occurs is not well formed, and therefore not part of the concrete specification.

The check that condition (25) is satisfied for a state machine \( P \), we may for instance, check that the traces obtained by transforming two corresponding events of the event library are not affected by the context they appear in, i.e.,
\[
((l, m), (?, m) \in \text{Dom}(\text{te}_{EL}) \wedge t \in [P \wedge ((l, m), (?, m)) \circ t) \Rightarrow \text{te}_{EL}((l, m), (?, m)) \Rightarrow (l' \in \text{te}_{EL}((l, m), (?, m)) : s' \prec t'))
\]
where \( s \prec t \) means that \( s \) is a sub-trace of \( t \), i.e., a trace \( s = (e_1, \ldots, e_n) \) is a sub-trace of \( t \) iff
\[
s_1 \prec (e_1) \cdots \prec s_n \prec (e_n) \prec s_{n+1} = t
\]
for some \( s_1, \ldots, s_{n+1} \in F^* \).

The conditions (23), (24), and (25) implies that (22) holds. By Theorem B.3 and definition of \( \text{NF} \), this means that any transformation satisfying (23), (24), and (25) will preserve the \( \text{NF} \) property.
C Syntactic categories

<table>
<thead>
<tr>
<th>Set*</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\mathcal{P}(A)$</td>
<td>${X \mid X \subseteq A}$ The power set of $A$.</td>
</tr>
<tr>
<td>$A^*$</td>
<td>The set of all finite sequences over the set $A$.</td>
</tr>
<tr>
<td>$ax \in$ AExp</td>
<td>Arithmetic expressions</td>
</tr>
<tr>
<td>$bx \in$ BExp</td>
<td>Boolean expressions</td>
</tr>
<tr>
<td>$sx \in$ SExp</td>
<td>String expressions</td>
</tr>
<tr>
<td>$ex \in$ Exp</td>
<td>$\text{Exp} \cup \text{AExp} \cup \text{BExp} \cup \text{SExp}$ Expressions</td>
</tr>
<tr>
<td>Val</td>
<td>$\text{Exp}$ The set of all values.</td>
</tr>
<tr>
<td>$x, y \in$ Var</td>
<td>$\text{Exp}$ The set of all variables.</td>
</tr>
<tr>
<td>$a \in$ Assign</td>
<td>$\text{Var} \times \text{Exp}$ The set of all assignments.</td>
</tr>
<tr>
<td>$nm \in$ Nm</td>
<td>$\text{Exp}$ The set of all names.</td>
</tr>
<tr>
<td>$si \in$ SI</td>
<td>$\text{Exp}$ The set of all signals.</td>
</tr>
<tr>
<td>$m \in$ M</td>
<td>$\text{Nm} \times \text{Nm} \times \text{SI}$ The set of all messages.</td>
</tr>
<tr>
<td>$ie \in$ IE</td>
<td>${?} \times M$ The set of all input events.</td>
</tr>
<tr>
<td>$oe \in$ OE</td>
<td>${!} \times M$ The set of all output events.</td>
</tr>
<tr>
<td>$e \in$ E</td>
<td>$\text{IE} \cup \text{OE}$ The set of all events.</td>
</tr>
<tr>
<td>$act \in$ Act</td>
<td>$\text{IE} \cup {\epsilon} \times \text{BExp} \cup {\epsilon} \times \text{OE} \cup {\epsilon} \times \text{Assign} \cup {\epsilon}$ The set of all actions.</td>
</tr>
<tr>
<td>$P \in$ P</td>
<td>Set of all basic state machine expressions.</td>
</tr>
<tr>
<td>$P \in$ PC</td>
<td>Set of all composite state machine expressions.</td>
</tr>
</tbody>
</table>

* By the notation $a \in A$ we understand that $A$ is ranged over by $a$. 
D Proofs

D.1 Auxiliary definitions

In this section, we make some definitions that are needed in the proofs.

**Definition D.1** We let the function \( | \cdot | : \mathcal{P} \rightarrow \mathcal{P}(\mathbb{Act}^*) \) yield the set of action sequences described by a basic state machine without potential choice. This function is defined by

\[
\begin{align*}
|\text{act}| & \equiv \{\text{act}\} \\
|P \cdot Q| & \equiv |P| \cdot |Q| \\
|P + Q| & \equiv |P| \cup |Q| \\
|P^*| & \equiv \bigcup_{\text{en}(P)}^i
\end{align*}
\]

**Definition D.2** We let the functions \( \text{preState}(\_), \text{postState}(\_) \in (\mathbb{Act} \times \hat{\Sigma} \times \hat{\Sigma})^* \rightarrow \hat{\Sigma} \) yield the first and the last state in a non-empty sequence of action-state triples, respectively. These functions are defined by

\[
\begin{align*}
\text{preState}((\text{act}, \sigma, \sigma')) & \equiv \sigma \\
\text{postState}(\sigma, ((\sigma, \sigma')) & \equiv \sigma'
\end{align*}
\]

In addition, we let the functions \( \text{preState}(\_), \text{postState}(\_), \in (\mathbb{Act} \times \hat{\Sigma} \times \hat{\Sigma})^* \times \mathcal{P}(\text{Var}) \rightarrow \hat{\Sigma} \) be defined by

\[
\begin{align*}
\text{preState}(\sigma, \text{V}) & \equiv \text{preState}(\sigma) \setminus (\text{V} \times \text{Val}) \\
\text{postState}(\sigma, \text{V}) & \equiv \text{postState}(\sigma) \setminus (\text{V} \times \text{Val})
\end{align*}
\]

**Definition D.3** We let \( \text{pr}(\_\_) \) be the function that yields the prefix closure of a set of sequences. The function is defined by

\[
\text{pr}(A) \equiv \{ s | t \in A \land s \subseteq t \}
\]

where \( \subseteq \), the prefix predicate, is defined by

\[ s \subseteq t \iff \exists u : s - u = t \]

D.2 Basic transformations

**Theorem B.1** The relation \( [T_{EL}] \) is a function.

**Proof of Theorem B.1**

**Assume:** 1. \( [P] = [Q] \) for some \( P, Q \in \mathcal{P} \)

2. \( EL \in EL \)

**Prove:** \( [T_{EL}(P)] = [T_{EL}(Q)] \)

(1) 1. **Assume:** 1.1. \( EL_1 \) = \( rnm(P, EL) \) and \( EL_2 = rnm(Q, EL) \) for \( EL_1, EL_2 \in EL \)

**Prove:** \( [T'_{EL_1}(P)] = [T'_{EL_2}(Q)] \)

(2) 1. **Assume:** 2.1. \( s' \in [T'_{EL_1}(P)] \)

**Prove:** \( s' \in [T'_{EL_2}(Q)] \)

(3) 1. Choose \( as_1 \in [T_{EL_1}(P)] \cap A \) such that \( io(as_1) = s' \)
PROOF: By assumption 2.1 and definition of $\mathcal{I}$ (Def. A.4).

(3.2) Choose $(act_{1,1}, \ldots, act_{1,j}) \in \mathcal{P}$ and $a_{1,1}^i \in \{T_{EL}^{\mathcal{P}}(act_{1,1})\}, \ldots, a_{1,j}^i \in \{T_{EL}^{\mathcal{P}}(act_{1,j})\}$ such that $a_{1,1}^i = a_{1,1} \cdots a_{1,j}$

PROOF: By (3.1) and Lemma T.B.1.1.

(3.3) Choose $a_{s1} \in \mathcal{A}$ and $a_{s1,1} \in \{act_{1,1}\}, \ldots, a_{s1,j} \in \{act_{1,j}\}$ such that

$a_{s1} = a_{s1,1} \cdots a_{s1,j}$ and for $i \in \{1, \ldots, j\}$: $\text{preState}(a_{s1,1}, \text{var}(EL_1)) = \text{postState}(a_{s1,1}, \text{var}(EL_1)) \wedge \text{postState}(a_{s1,1}, \text{var}(EL_1)) = \text{preState}(a_{s1,1}, \text{var}(EL_1))$

PROOF: By assumptions 1.1 and 2, (3.1), (3.2), and Lemma T.B.1.2.

(3.4) $\text{io}(a_{s1}) \in \{P\}$

PROOF: By (3.2), (3.3), definition of $\mathcal{I}$ (Def. A.3), definition of $\mathcal{I}$ (Def. A.4), and definition of $\text{var}$ and $\text{run}$ (App. B.1).

(3.5) Choose $a_{s2} \in \{\mathcal{Q}\} \cap \mathcal{A}$ such that $\text{io}(a_{s2}) = \text{io}(a_{s1})$

PROOF: By (3.4), assumption 1 and definition of $\mathcal{I}$ (Def. A.4).

(3.6) Choose $(act_{2,1}, \ldots, act_{2,k}) \in \mathcal{Q}$ and $a_{s2,1} \in \{act_{2,1}\}, \ldots, a_{s2,k} \in \{act_{2,k}\}$ such that $a_{s2} = a_{s2,1} \cdots a_{s2,k}$

PROOF: By (3.5), definition of $\mathcal{I}$ (Def. D.1) and definition of $\mathcal{I}$ (Def. A.3).

(3.7) Choose $a_{s2}^i \in \{T_{EL}^{\mathcal{P}}(act_{2,1})\} \cdots \{T_{EL}^{\mathcal{P}}(act_{2,k})\}$ such that $a_{s2}^i \in \mathcal{A}$ and $\text{io}(a_{s2}^i) = \text{io}(a_{s1}^i)$

PROOF: By assumption 1 and 1.1, (3.1), (3.2), (3.3), (3.4), (3.5), (3.6), and Lemma T.B.1.3.

(3.8) $a_{s2} \in \{T_{EL}^{\mathcal{Q}}(\mathcal{Q})\} \cap \mathcal{A}$

PROOF: By (3.6), (3.7), and definition of $\mathcal{I}$ (Def. A.3) and definition of $\mathcal{T_{EL}^{\mathcal{Q}}}$ (Def. B.3).

(3.9) Q.E.D.

PROOF: By (3.1), (3.7), (3.8) and definition of $\mathcal{I}$ (Def. A.4).

(2.2) ASSUME: $1.1 s' \in \{T_{EL}^{\mathcal{P}}(P)\}$

PROOF: $s' \in \{T_{EL}^{\mathcal{P}}(P)\}$

PROOF: By (2.1) and symmetry of $=.$

(2.3) Q.E.D.

PROOF By (2.1) and (2.2).

(1.2) Q.E.D.

PROOF: By (1.1) and definition of $T_{EL}$ (Def. B.3).

Lemma T.B.1.1 If

- $a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(P)\}$

then

- $\exists (act_1, \ldots, act_n) \in \mathcal{P}: \exists a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(act_1)\} \cdots \exists a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(act_n)\} : a_{s2} = a_{s2}^1 \cdots a_{s2}^n$

Proof of Lemma T.B.1.1

ASSUME: $1. EL \in \mathcal{EL}$ and $P \in \mathcal{P}$

PROOF: $a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(P)\} \Rightarrow (\exists (act_1, \ldots, act_n) \in \mathcal{P}: \exists a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(act_1)\} \cdots \exists a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(act_n)\} : a_{s2} = a_{s2}^1 \cdots a_{s2}^n)$

(1.1) CASE: $1.1 P = act$

(2.1) ASSUME: $2.1 a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(act)\}$

PROOF: $\exists (act_1, \ldots, act_n) \in \mathcal{act}: \exists a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(act)\} \cdots \exists a_{s2}^i \in \{T_{EL}^{\mathcal{Q}}(act)\} : a_{s2} = a_{s2}^1 \cdots a_{s2}^n$
(3.1) Choose \( \langle \text{act}_1 \rangle \in |\text{act}| \) 
**Proof:** By definition of \( |\_| \) (Def. D.1).

(3.2) \( \text{as}' \in \langle T'_{\text{EL}}(\text{act}) \rangle \) 
**Proof:** By (3.1) and assumption 2.1.

(3.3) Q.E.D. 
**Proof:** By (3.1) and (3.2).

(2.2) Q.E.D. 
**Proof:** By (2.1) and logical implication.

(1.2) **Case:** \( 1.1 \ P = P_1 \cdot P_2 \)

1.2. \( \text{as}' \in \langle T'_{\text{EL}}(P_1) \rangle \) \( \Rightarrow \) \( \exists \langle \text{act}_1, \ldots, \text{act}_n \rangle \in |P_1| : \exists \text{as}' \in \langle T'_{\text{EL}}(\text{act}_1) \rangle \ldots \exists \text{as}' \in \langle T'_{\text{EL}}(\text{act}_n) \rangle : \text{as}' = \text{as}'_1 \cdots \text{as}'_n 

(3.1) **Choose** \( \text{as}'_1 \in \langle T'_{\text{EL}}(P_1) \rangle \) and \( \text{as}'_2 \in \langle T'_{\text{EL}}(P_2) \rangle \) such that \( \text{as}' = \text{as}'_1 \cdots \text{as}'_2 \) 
**Proof:** By assumption 2.1, definition of \( T'_{\text{EL}} \) (Def. B.3), and definition of \( (\_)_i \) (Def. A.3).

(3.2) **Choose** \( \langle \text{act}_1, \ldots, \text{act}_1, \text{act}_{1,i} \rangle \in |P_1| \) and \( \text{as}'_1 \in \langle T'_{\text{EL}}(\text{act}_1) \rangle \ldots \text{as}'_{1,i} \in \langle T'_{\text{EL}}(\text{act}_{1,i}) \rangle \) such that \( \text{as}' = \text{as}'_1 \cdots \text{as}'_{1,i} \) 
**Proof:** By (3.1) and assumption 1.2.

(3.3) **Choose** \( \langle \text{act}_2, \ldots, \text{act}_{2,k} \rangle \in |P_2| \) and \( \text{as}'_1 \in \langle T'_{\text{EL}}(\text{act}_{2,1}) \rangle \ldots \text{as}'_{2,k} \in \langle T'_{\text{EL}}(\text{act}_{2,k}) \rangle \) such that \( \text{as}' = \text{as}'_{2,1} \cdots \text{as}'_{2,k} \) 
**Proof:** By (3.1) and assumption 1.3.

(3.4. \langle \text{act}_1, \ldots, \text{act}_{1,i}, \text{act}_{2,1}, \ldots, \text{act}_{2,k} \rangle \in |P_1| \cdot |P_2| 
**Proof:** By (3.2), (3.3) and definition of \( |\_| \) (Def. D.1).

(3.5) Q.E.D. 
**Proof:** By (3.1) - (3.4).

(2.2) Q.E.D. 
**Proof:** By (2.1) and logical implication.

(1.3) **Case:** \( 1.1 \ P = P_1 + P_2 \)

1.3. \( \text{as}' \in \langle T'_{\text{EL}}(P_1) \rangle \) \( \Rightarrow \) \( \exists \langle \text{act}_1, \ldots, \text{act}_n \rangle \in |P_1| : \exists \text{as}' \in \langle T'_{\text{EL}}(\text{act}_1) \rangle \ldots \exists \text{as}' \in \langle T'_{\text{EL}}(\text{act}_n) \rangle : \text{as}' = \text{as}'_1 \cdots \text{as}'_n 

(2.1) Assume: \( 2.1 \ \text{as}' \in \langle T'_{\text{EL}}(P_1 + P_2) \rangle \) 
**Proof:** \( \exists \langle \text{act}_1, \ldots, \text{act}_n \rangle \in |P_1 + P_2| : \exists \text{as}' \in \langle T'_{\text{EL}}(\text{act}_1) \rangle \ldots \exists \text{as}' \in \langle T'_{\text{EL}}(\text{act}_n) \rangle : \text{as}' = \text{as}'_1 \cdots \text{as}'_n 

(3.1) **Choose** \( i \in \{1, 2\} \) such that \( \text{as}' \in \langle T'_{\text{EL}}(P_i) \rangle \) 
**Proof:** By assumption 2.1 and definition of \( T'_{\text{EL}} \) (Def. B.3), and definition of \( \emptyset \) (Def. A.3).

(3.2) **Choose** \( \langle \text{act}_1, \ldots, \text{act}_n \rangle \in |P_i| \) and \( \text{as}'_1 \in \langle T'_{\text{EL}}(\text{act}_1) \rangle \ldots \text{as}'_n \in \langle T'_{\text{EL}}(\text{act}_n) \rangle \) such that \( \text{as}' = \text{as}'_1 \cdots \text{as}'_n \) 
**Proof:** By (3.1) and assumption 1.2 or assumption 1.3.

(3.3) \( \langle \text{act}_1, \ldots, \text{act}_n \rangle \in |P_1 + P_2| 
**Proof:** By (3.2) and \( |\_| \) (Def. D.1).

(3.4) Q.E.D. 
**Proof:** By (3.1) - (3.3).
(2.2) Q.E.D.

Proof: By (2.1) and logical implication.

(1.4) Case: 1: $P = P_1$

1.2 $as' \in \{T'_{EL}(P_1)\}$ \implies exists (act_1, \ldots, act_n) \in |P_1| : \exists as'_{i-1} \in \\
\{T'_{EL}(act_1)\} : \ldots : \exists as'_{n-1} \in \\
\{T'_{EL}(act_n)\} : as' = as'_{1-1} \cdots as'_{n-1}$

(2.1) Assume: 2.1 $as' \in \{T'_{EL}(P_1)\}$

Proof: exists (act_1, \ldots, act_n) \in |P_1| : \exists as'_{i-1} \in \\
\{T'_{EL}(act_1)\} : \ldots : \exists as'_{n-1} \in \\
\{T'_{EL}(act_n)\} : as' = as'_{1-1} \cdots as'_{n-1}$

(3.1) Choose $j \in \mathbb{N} \setminus \{0\}$ such that $as' \in \{T'_{EL}(P_1)\}^j$

Proof: By assumption 2.1 and definition of $T'_{EL}$ (Def. B.3), and definition of $\{\}$ (Def. A.3).

(3.2) Case: 3.1 $j = 1$

4.1. $as' \in \{T'_{EL}(P_1)\}^1$

Proof: By (3.1), and assumption 3.1.

4.2. Choose (act_1, \ldots, act_n) \in |P_1| and as'_{i-1} \in \{T'_{EL}(act_1)\} : \ldots : as'_{n-1} \in \\
\{T'_{EL}(act_n)\}$ such that $as' = as'_{1-1} \cdots as'_{n-1}$

Proof: By (4.1) and assumption 1.2.

4.3. (act_1, \ldots, act_n) \in |P_1|

Proof: By (4.2) and definition of $|\|$ (Def. D.1).

4.4. Q.E.D.

Proof: By (4.2) and (4.3).

(3.3) Case: 3.1 $j = j + 1$

3.2 \forall i \leq j' : as' \in \{T'_{EL}(P_1)\}^{j'} \implies \exists (act_1, \ldots, act_n) \in \\
|P_1| : \exists as'_{1-1} \in \{T'_{EL}(act_1)\} : \ldots : \exists as'_{n-1} \in \\
\{T'_{EL}(act_n)\} : as' = as'_{1-1} \cdots as'_{n-1}$

4.1. Choose as'_{1} \in \{T'_{EL}(P_1)\}^{j'}$ and as'_{2} \in \{T'_{EL}(P_1)\}$ such that $as' = as'_{1-1} \cdots as'_{2-1}$

Proof: By (3.1), assumption 3.1, definition of $T'_{EL}$ (Def. B.3), and definition of $\{\}$ (Def. A.3).

4.2. Choose (act_1, \ldots, act_{1,m}) \in |P_1|^{j'} and as'_{1-1} \in \{T'_{EL}(act_{1,1})\} : \ldots : as'_{n,m} \in \\
\{T'_{EL}(act_{1,m})\}$ such that $as'_{1} = as'_{1-1} \cdots as'_{1,m}$

Proof: By (4.1) and assumption 3.2.

4.3. Choose (act_{2,1}, \ldots, act_{2,n}) \in |P_1| and as'_{2-1} \in \{T'_{EL}(act_{2,1})\} : \ldots : as'_{n,2} \in \\
\{T'_{EL}(act_{2,n})\}$ such that $as'_{2} = as'_{2-1} \cdots as'_{n,2}$

Proof: By (4.1) and assumption 1.2.

4.4. (act_1, \ldots, act_{1,m}, act_{2,1}, \ldots, act_{2,n}) \in |P_1|

Proof: By (4.2), (4.3) and definition of $|\|$ (Def. D.1).

4.5. Q.E.D.

Proof: By (4.2) - (4.4).

3.4. Q.E.D.

Proof: By (3.2) and (3.3).

(2.2) Q.E.D.

Proof: By (2.1) and logical implication.

1.5. Q.E.D.

Proof: By (1.1) - (1.4) and definition of $P$ (Def. A.1).

Lemma T.3.1.2 If

* $\text{var}(EL) \cap (\text{var}(act_1) \cup \cdots \cup \text{var}(act_n)) = \emptyset$
* $as' \in A$
• \(a_1' \in \{T_{EL}(act_1)\}, \ldots, a_n' \in \{T_{EL}(act_n)\}\)
• \(a' = a_1' \cdot \cdots \cdot a_n'\)

then

• \(\exists a \in A : \exists a_1 \in \{act_1\} : \cdots : \exists a_n \in \{act_n\} : \forall i \in \{1, \ldots, n\} : \text{preState}(a_i, \text{var}(EL)) = \text{preState}(a_i', \text{var}(EL)) \land \text{postState}(a_i, \text{var}(EL)) = \text{postState}(a_i', \text{var}(EL))\)

**Proof of Lemma T.B.1.2** The proof is by induction on the length of the action sequence \((act_1, \ldots, act_n)\). In the proof we make use of the following definition which high-lights the induction.

\[
\text{Ind}((act_1, \ldots, act_n), EL) \iff \\
\forall a \in A : \forall a_1 \in \{T_{EL}(act_1)\} : \cdots : \forall a_n \in \{T_{EL}(act_n)\} : \\
a' = a_1' \cdots a_n' \\
\implies \exists a \in A : \\
\exists a_1 \in \{act_1\} : \cdots : \exists a_n \in \{act_n\} : \\
a = a_1 \cdot \cdots \cdot a_n \\
\land \forall i \in \{1, \ldots, n\} : \\
\land \text{preState}(a_i, \text{var}(EL)) = \text{preState}(a_i', \text{var}(EL)) \\
\land \text{postState}(a_i, \text{var}(EL)) = \text{postState}(a_i', \text{var}(EL))
\]

Also, if \(s\) is a sequence, we denote by \(\text{pref}(s, j)\), the prefix of \(s\) of length \(j\). If \(j\) is greater than the length of \(s\), then \(\text{pref}(s, j) = s\).

The proof is given by the following.

**Assume:** \(\text{var}(EL) \cap (\text{var}(act_1) \cup \cdots \cup \text{var}(act_n)) = \emptyset\) for \(EL \in EL\) and \(act_1, \ldots, act_n \in \text{Act}\) and \(n \geq 1\)

**Prove:** \(\text{Ind}((act_1, \ldots, act_n), EL)\)

1. **Case:** 1.1 \(n = 1\)
2.1. **Assume:** 2.1 \(a_1' \in A\)
2.2. \(a_1' \in \{T_{EL}(act_1)\}\)
2.3. \(a' = a_1'\)

**Prove:** 3. \(a \in A : \exists a_1 \in \{act_1\} : a = a_1 \land \text{preState}(a_i, \text{var}(EL)) = \text{preState}(a_1', \text{var}(EL)) \\
\land \text{postState}(a_i, \text{var}(EL)) = \text{postState}(a_1', \text{var}(EL))\)

3.1. **Case:** 3.1 \(act_1 = \{e, bx, e, a\}\) for \(bx \in \text{BExp} \cup \{\epsilon\}\) and \(a \in \text{Assign} \cup \{\epsilon\}\)

(4.1). **Choose** \(a \in \{act_1\}\) such that \(a = a'\)

(4.2). \(T_{EL}(act_1) = \{\text{tact}_{EL}(act_1)\} = \{act_1\}\)

**Proof:** By assumption 3.1, definition of \(T_{EL}\) (Def. B.3), and definition of \(\text{tact}_{EL}\) (Eq. (15)), definition of \(\text{tact}_{EL}\) (Eq. (14)), and definition of \(\text{BExp}\) (App. A.1.1).

(5). **Q.E.D.**

**Proof:** By assumption 2.2, assumption 2.3, and (5.1).

(4.2). \(a \in A\)

**Proof:** By (4.1) and assumption 2.1.

(4.3). \(\text{preState}(as, \text{var}(EL)) = \text{preState}(as', \text{var}(EL))\) and \(\text{postState}(as, \text{var}(EL)) = \text{postState}(as', \text{var}(EL))\)

**Proof:** By (4.1).

(4.4). **Q.E.D.**
Proof: By (41), (4)2, and (4)3.

(3)2. Case: 3.1 \( \text{act}_1 = \{ i, b_{x_i}, e, a_{c} \} \) for \( i \in \text{IE}, b_{x_i} \in \text{BExp} \cup \{ e \} \), and \( a_{c} \in \text{Assign} \cup \{ e \} \)

(4)1. Case: 4.1 \( i \in \text{Dom}(\text{te}_{\text{EL}}) \)

(5)1. Choose \( \text{as}_1 \in \{ \{ e, b_{x_i}, e, c \} \} \), \( \text{as}_2' \in \{ \text{te}_{\text{EL}}(i) \} \), and \( \text{as}_3' \in \{ \{ e, e, e, a_{c} \} \} \) such that \( \text{as}_1' = \text{as}_1 \bigcirc \text{as}_2' \bigcirc \text{as}_3' \)

Proof: By assumption 2.2, assumption 3.1, assumption 4.1, and definition of \( \text{te}_{\text{EL}}' \) (Def. B.3), and (4) (Def. A.3).

(5)2. Choose \( \text{as} \in \{ \{ i, b_{x_i}, e, a_{c} \} \} \) such that \( \text{preState}(\text{as}, \text{var}(\text{EL})) = \text{preState}(\text{as}_1', \text{var}(\text{EL})) \) and \( \text{postState}(\text{as}, \text{var}(\text{EL})) = \text{postState}(\text{as}_2', \text{var}(\text{EL})) \)

Proof: By assumption 1 and (5)1, definition of \( \bigcirc \) (Def. A.3), definition of \( \text{te}_{\text{EL}} \) (Eq. 14), and definition of \( \text{var}(\text{EL}) \) (Section B.1).

(5)3. \( \text{as} \in \mathcal{A} \)

Proof: By assumptions 2.1, 2.2, and 2.3, and (5)1, (5)2, and definition of \( \mathcal{A} \) (Eq. 10).

(5)4. Q.E.D.

Proof: By (5)2, assumption 3.1, and (5)3.

(4)2. Case: 4.1 \( i \notin \text{Dom}(\text{te}_{\text{EL}}) \)

(5)1. Choose \( \text{as} \in \{ \text{act}_1 \} \) such that \( \text{as} = \text{as}' \)

(6)1. \( \{ \text{te}_{\text{EL}}(\text{act}_1) \} = \{ \text{tact}_{\text{EL}}(\text{act}_1) \} = \{ \text{act}_1 \} \)

Proof: By assumption 3.1, assumption 4.1, definition of \( \text{te}_{\text{EL}}' \) (Def. B.3), definition of \( \text{tact}_{\text{EL}} \) (Eq. 15), definition of \( \text{te}_{\text{EL}} \) (Eq. 14), and definition of \( \mathcal{E} \) (App. A.1.1).

(6)2. Q.E.D.

Proof: By assumption 2.2, assumption 2.3, and (6)1.

(5)2. \( \text{as} \in \mathcal{A} \)

Proof: By (5)1 and assumption 2.1.

(5)3. \( \text{preState}(\text{as}, \text{var}(\text{EL})) = \text{preState}(\text{as}', \text{var}(\text{EL})) \) and \( \text{postState}(\text{as}, \text{var}(\text{EL})) = \text{postState}(\text{as}', \text{var}(\text{EL})) \)

Proof: By (5)2.

(5)4. Q.E.D.

Proof: By (5)1, (5)2, and (5)3.

(4)3. Q.E.D.

Proof: By (4)1 and (4)2.

(3)3. Case: 3.1 \( \text{act}_1 = \{ e, b_{x_i}, oc, a_{c} \} \) for \( oc \in \text{OE}, b_{x_i} \in \text{BExp} \cup \{ e \} \), and \( a_{c} \in \text{Assign} \cup \{ e \} \)

Proof: Proof is the same as for (3)2.

(3)4. Q.E.D.

Proof: By (3)1 - (3)3 and definition of \( \text{Act} \) (Eq. 7) and (Eq. 8).

(2)2. Q.E.D.

Proof: By (2)1 and logical implication.

(1)2. Case: 1.1 \( n = n + 1 \)

(2)1. Assume: 2.1 \( \text{as} \in \mathcal{A} \)

2.2 \( \text{as}_1 = \{ \text{te}_{\text{EL}}(\text{act}_1) \} \) and \( \text{as}_n = \{ \text{te}_{\text{EL}}(\text{act}_n) \} \)

2.3 \( \text{as}' = \text{as}_1 \bigcirc \cdots \bigcirc \text{as}_n \)

Proof: \( \exists \text{as} \in \mathcal{A} : \exists \text{as}_1 \in \{ \text{act}_1 \} : \cdots : \exists \text{as}_n \in \{ \text{act}_n \} : \text{as} = \text{as}_1 \bigcirc \cdots \bigcirc \text{as}_n \) and \( (\forall i \in \{ 1, \ldots, n \}) : \text{preState}(\text{as}_i, \text{var}(\text{EL})) = \text{preState}(\text{as}', \text{var}(\text{EL})) \bigcirc \text{postState}(\text{as}_i, \text{var}(\text{EL})) = \text{postState}(\text{as}', \text{var}(\text{EL})) \)

(3)1. Choose \( \text{as}' \in \mathcal{A} \) such that \( \text{as}' = \text{as}_1 \bigcirc \cdots \bigcirc \text{as}_n \)
PROOF: By assumption 2.1, assumption 2.3, and definition of $A$ (Eq. (10)).

(3.2) Choose $aas \in A$ and $a \in \{act_1\}, \ldots, a_{n-1} \in \{act_{n-1}\}$ such that $aas = (a_1, \ldots, a_{n-1})$ and $Y_i \in \{1, \ldots, n-1\}$: $\text{preState}(a_{Y_i}, \text{var}(EL)) = \text{preState}(a_{Y_i}, \text{var}(EL))$ and $\text{postState}(a_{Y_i}, \text{var}(EL)) = \text{postState}(a_{Y_i}, \text{var}(EL))$

PROOF: By (3.1) and assumptions 1.1, 1.2, 2.2, and 2.3.

(3.3) Choose $a_{n} \in \{act_n\}$ such that $aas - a_{n} \in A$, $\text{preState}(a_{n}, \text{var}(EL)) = \text{postState}(aas, \text{var}(EL))$, $\text{preState}(a_{n}, \text{var}(EL)) = \text{preState}(a_{n}, \text{var}(EL))$, and $\text{postState}(a_{n}, \text{var}(EL)) = \text{postState}(a_{n}, \text{var}(EL))$

(4.1) Choose $a_{n} \in \{act_n\}$ such that $\text{preState}(a_{n}, \text{var}(EL)) = \text{postState}(aas, \text{var}(EL))$

PROOF: By (3.2) and definition of $\{\emptyset\}$ (Def. A.3).

(4.2) $\text{preState}(a_{n}, \text{var}(EL)) = \text{preState}(a_{n}, \text{var}(EL))$

(5.1) $\text{postState}(aas, \text{var}(EL)) = \text{postState}(a_{n-1}, \text{var}(EL))$

PROOF: By (3.2) and definition of postState (Def. D.2).

(5.2) $\text{postState}(a_{n-1}, \text{var}(EL)) = \text{postState}(a_{n}, EL)$

PROOF: By assumptions 2.1 - 2.3 and definition of $A$ (Eq. (10)).

(5.3) Q.E.D.

PROOF: By (4.1), (5.1), and (5.2).

(4.3) $\text{postState}(a_{n}, \text{var}(EL)) = \text{postState}(a_{n}, \text{var}(EL))$

(5.1) CASE: $3.1 \text{act}_n = (\epsilon, bx, a_i)$ for $bx \in \text{BExp} \cup \{\epsilon\}$ and $a_i \in \text{Assign} \cup \{\epsilon\}$

(6.1) $a_{n} \in \{act_n\} = \{\text{TE}_L(a_{n})\}$

PROOF: By assumption 2.2, assumption 3.1, and definition of $\text{TE}_L$

(6.2) Q.E.D.

PROOF: By assumption 1, (4.1), (4.2), (6.1), and definition of $\{\emptyset\}$ (Def. A.3).

(6.2) CASE: $3.1 \text{act}_n = (\epsilon, bx, a_i)$ for $bx \in \text{BExp} \cup \{\epsilon\}$, and $a_i \in \text{Assign} \cup \{\epsilon\}$

(6.1) CASE: $4.1 \epsilon \in \text{Dom}(\text{TE}_L)$

(7.1) Choose $aas' \in \{(\epsilon, bx, \epsilon, c), aas' \in \{\text{TE}_L(\epsilon, i)\}$, and $aas' \in \{(\epsilon, \epsilon, c, a_i)\}$ such that $a_{n} = aas' = aas' = aas' = aas' = aas'$

PROOF: By assumption 2.2, assumption 3.1, assumption 4.1, and definition of $\text{TE}_L$ (Def. B.3).

(7.2) $\text{postState}(a_{n}, \text{var}(EL)) = \text{postState}(a_{n}, \text{var}(EL))$

PROOF: By assumption 1, assumption 3.1, (4.1), (4.2), (7.1), and definition of $\{\emptyset\}$ (Def. A.3).

(7.3) Q.E.D.

PROOF: By (7.1) and (7.2).

(6.2) CASE: $4.1 \epsilon \notin \text{Dom}(\text{TE}_L)$

(7.1) $a_{n} \in \{act_n\} = \{\text{TE}_L(a_{n})\}$

PROOF: By assumption 2.2, assumption 3.1, and definition of $\text{TE}_L$ (Def. B.3).

(7.2) Q.E.D.

PROOF: By assumption 1, (4.1), (4.2), (7.1), and definition of $\{\emptyset\}$ (Def. A.3).

(7.3) Q.E.D.

PROOF: By (6.1) and (6.2).

(5.3) CASE: $3.1 \text{act}_n = (\epsilon, bx, a_i)$ for $bx \in \text{BExp} \cup \{\epsilon\}$, and $a_i \in \text{Assign} \cup \{\epsilon\}$
Proof: Proof is the same as for (5)2.
(5)4. Q.E.D.

Proof: By (3)1 - (5)3 and definition of Act (Eq. (7)) and (Eq. (8)).
(4)4. as \sim as_n \in A
Proof: By assumption 2.1 - 2.3, (3)2, (4)1, (4)2, assumption 1, definition of \cdot T_{EL}^r (Def. B.3), and definition of A (Def. 10).
(4)5. Q.E.D.
Proof: By (4)1 - (4)4.
(3)4. Q.E.D.
Proof: By (3)1, (3)2 and (3)3.
(2)2. Q.E.D.

Proof: By (2)1 and logical implication.
(1)3. Q.E.D.
Proof: By (1)1 and (1)2.

Lemma T.B.1.3 If

- \cdot EL_1 = rnm(P, EL) and \cdot EL_2 = rnm(Q, EL) for some P, Q \in P and EL, EL_1, EL_2 \in EL
- \langle \cdot act_1, \ldots , \cdot act_1, j \rangle \in pr(\{P\})
- \cdot as_{1,1} = \cdot T_{EL_1}(\cdot act_1, j) \wedge \cdots \wedge \cdot as_{1, j} = \cdot T_{EL_1}(\cdot act_1, j)
- \cdot as_1 = \cdot T_{EL_1}(\cdot act_1, j) \wedge \cdots \wedge \cdot as_{1, j}
- \cdot \forall i \in \{1, \ldots , j\} : (\cdot preState(as_{1, i}, var(EL_1)) = \cdot preState(as_{1, i}, var(EL_1))) \wedge (\cdot postState(as_{1, i}, var(EL_1)) = \cdot postState(as_{1, i}, var(EL_1)))
- \cdot as \in pr(\{P\}) \cap A and as_{1,1} = \cdots = \cdot as_{1, j}
- \cdot as_2 \in pr(\{Q\}) \cap A and \cdot io(as_2) = \cdot io(as_1)
- \langle \cdot act_2, \ldots , \cdot act_2, k \rangle \in pr(\{Q\})
- \cdot as_{2,1} = \cdot act_2, \ldots , \cdot as_{2, k} \in \cdot act_2, k
- \cdot as_2 = \cdot as_{2,1} \cdots \cdot as_{2, k}

then

- \exists \cdot as_{2} = \cdot T_{EL_2}(\cdot act_2, k) \cdots \cdot \cdot T_{EL_2}(\cdot act_2, k) : \cdot as_2 \in A \wedge \cdot io(as_2) = \cdot io(as_1)

Proof of Lemma T.B.1.3 The proof is by induction on the sum j and k. In the proof, we make use of the following definition which high-lights the
induction.

\[ \text{Ind}(P, Q, EL_1, EL_2, n) \iff \]
\[ \forall j, k \in \mathbb{N} \setminus \{0\} : \]
\[ \forall (act_1, \ldots, act_{j+1}) \in \text{pr}(|P|) : \]
\[ \forall \bar{a}_{1,j} \in \{ T_{EL_1}(act_1, 1) \} : \cdots \forall \bar{a}_{1,j} \in \{ T_{EL_1}(act_{j+1}, 1) \} : \]
\[ \forall \bar{a}_{2,k} \in \{ act_{k, 2} \} : \cdots : \forall \bar{a}_{2,k} \in \{ act_{j+1, 2} \} : \]
\[ \forall \bar{a}'_{1,j} \in \mathcal{A} : \forall \bar{a}_1 \in \text{pr}(|P|) \cap \mathcal{A} : \forall \bar{a}_2 \in \text{pr}(|Q|) \cap \mathcal{A} : \]
\[ \wedge j + k = n \]
\[ \wedge \bar{a}'_{1,j} = \bar{a}_{1,j} \cdots \bar{a}_{1,j} \]
\[ \wedge \bar{a}_1 = \bar{a}_{1,1} \cdots \bar{a}_{1,j} \]
\[ \wedge \bar{a}_2 = \bar{a}_{2,1} \cdots \bar{a}_{2,k} \]
\[ \wedge \text{io}(\bar{a}_1) = \text{io}(\bar{a}_2) \]
\[ \wedge \forall i \in \{1, \ldots, j\} : \]
\[ \wedge \text{preState}(\bar{a}_{1,i}, \text{var}(EL_1)) = \text{preState}(\bar{a}'_{1,i}, \text{var}(EL_1)) \]
\[ \wedge \text{postState}(\bar{a}_{1,i}, \text{var}(EL_1)) = \text{postState}(\bar{a}'_{1,i}, \text{var}(EL_1)) \]
\[ \Rightarrow \exists \bar{a}_2' \in \{ T_{EL_2}(act_{k, 2}) \} : \cdots \exists \bar{a}_2' \in \{ T_{EL_2}(act_{j+1, 2}) \} : \]
\[ \wedge \bar{a}_2' \in \mathcal{A} \]
\[ \wedge \text{io}(\bar{a}_2') = \text{io}(\bar{a}_2) \]

ASSUME: \( EL_1 = \text{run}(P, EL) \) and \( EL_2 = \text{run}(Q, EL) \) for some \( P, Q \in P \) and \( EL, EL_1, EL_2 \in EL \)

2. \( n \geq 2 \) for some \( n \in \mathbb{N} \)

PROVE: \( \text{Ind}(P, Q, EL_1, EL_2, n) \)

(1). CASE: 1. \( n = 2 \)

(2). Assume: 2.1 \( j = 1 \) and \( k = 1 \) for \( j, k \in \mathbb{N} \setminus \{0\} \)

2.2 \( (act_1) \in \text{pr}(|P|) \)

2.3 \( \bar{a}_1 \in \{ T_{EL_1}(act_1) \} \)

2.4 \( \bar{a}_1 \in \mathcal{A} \)

2.5 \( \bar{a}_1 \in \{ act_1 \} \)

2.6 \( \text{preState}(\bar{a}_1, \text{var}(EL_1)) = \text{preState}(\bar{a}_1, \text{var}(EL_1)) \)

\[ \land \text{postState}(\bar{a}_1, \text{var}(EL_1)) = \text{postState}(\bar{a}_1, \text{var}(EL_1)) \]

2.7 \( \bar{a}_1 \in \text{pr}(|P|) \cap \mathcal{A} \)

2.8 \( \bar{a}_2 \in \text{pr}(|Q|) \cap \mathcal{A} \) and \( \text{io}(\bar{a}_1) = \text{io}(\bar{a}_2) \)

2.9 \( (act_2) \in \text{pr}(|Q|) \)

2.10 \( \bar{a}_2 \in \{ act_2 \} \)

PROVE: \( \exists \bar{a}_2' \in \{ T_{EL_2}(act_2) \} : \bar{a}_2' \in \mathcal{A} \land \text{io}(\bar{a}_2') = \text{io}(\bar{a}_2) \)

(3). CASE: 3.1 \( act_1 = (e, bx, e, a, e) \) for \( bx, e \in \text{BExp} \cup \{e\} \) and \( a, e \in \text{Assign} \cup \{e\} \)

(4). CASE: 4.1 \( act_2 = (e, bx, e, a') \) for \( bx', e \in \text{BExp} \cup \{e\} \) and \( a, e \in \text{Assign} \cup \{e\} \)

(5). Choose \( \bar{a}_2' \in \{ T_{EL_2}(act_2) \} \) such that \( \bar{a}_2' = \bar{a}_2 \)

(6). \( \{ T_{EL_2}(act_2) \} = \{ \text{tact}_{EL_2}(act_2) \} = \{ act_2 \} \)

PROOF: By definition of \( T_{EL_2} \) (Def. B.3), definition of \( \text{tact}_{EL} \) (Eq. (19)), and assumption 4.1.

(6). Q.E.D.

PROOF: By (6) and assumption 2.10.
\(\{5\} 2. \text{as}_2 \in \mathcal{A} \)

**Proof:** By \(\{5\} 1\) and assumption 2.8.

\(\{5\} 3. \text{io}(\text{as}_2) = \text{io}(\text{as}_1)\)

**Proof:** By \(\{5\} 1\), assumption 2.10, assumption 4.1, definition of \(\cdot\) (Def. A.3) and definition of \text{io} (Eq. (11)).

\(\{6\} 1. \text{io}(\text{as}_1) = \emptyset\)

**Proof:** By assumption 2.3, assumption 3.1, definition of \(T_{EL_1}'\) (Def. B.3), definition of \(\cdot\) (Def. A.3) and definition of \text{io} (Eq. (11)).

\(\{6\} 3. \text{Q.E.D.}\)

**Proof:** By \(\{6\} 1\) and \(\{6\} 2\).

\(\{5\} 4. \text{Q.E.D.}\)

**Proof:** By \(\{5\} 1\) - \(\{5\} 3\).

\(\{4\} 2. \text{CASE:} 4.1 \text{act}_2 = \langle ic', bx', cc', aa' \rangle \text{ for } ic' \in \text{IE} \cup \{c\}, \ bx' \in \text{BExp} \cup \{c\}, \text{ and } aa' \in \text{Assign} \cup \{c\}\)

**Proof:** Case assumption 4.1 contradicts assumption 2.8 since assumption 4.1 implies that \(\text{io}(\text{as}_2) \neq \text{io}(\text{as}_1)\) by definition of \(\cdot\) (Def. A.3) and definition of \text{io} (Eq. (11)). Hence, assumption 4.1 cannot hold.

\(\{4\} 3. \text{CASE:} 4.1 \text{act}_2 = \langle c, bx', cc', aa' \rangle \text{ for } cc' \in \text{OE}(\{c\}), \ bx' \in \text{BExp} \cup c, \text{ and } aa' \in \text{Assign} \cup \{c\}\)

**Proof:** Case assumption 4.1 contradicts assumption 2.8 since assumption 4.1 implies that \(\text{io}(\text{as}_2) \neq \text{io}(\text{as}_1)\) by definition of \(\cdot\) (Def. A.3) and definition of \text{io} (Eq. (11)). Hence, assumption 4.1 cannot hold.

\(\{4\} 4. \text{Q.E.D.}\)

**Proof:** By \(\{4\} 1\) - \(\{4\} 3\) and definition of \text{Act} (Eq. 7) and (Eq. 8).

\(\{3\} 2. \text{CASE:} 3.1 \text{act}_1 = \langle ic, bx, cc, aa \rangle \text{ for } ic \in \text{IE} \cup \{c\}, \ bx \in \text{BExp} \cup \{c\}, \text{ and } aa \in \text{Assign} \cup \{c\}\)

**Proof:** Case assumption 4.1 contradicts assumption 2.8 since assumption 4.1 implies that \(\text{io}(\text{as}_2) \neq \text{io}(\text{as}_1)\) by definition of \(\cdot\) (Def. A.3) and definition of \text{io} (Eq. (11)). Hence, assumption 4.1 cannot hold.

\(\{4\} 2. \text{CASE:} 4.1 \text{act}_2 = \langle ic', bx', cc', aa' \rangle \text{ for } ic' \in \text{IE} \cup \{c\}, \ bx' \in \text{BExp} \cup c, \text{ and } aa' \in \text{Assign} \cup \{c\}\)

**Proof:** 5.1 \(ic \in \text{Dom}(\text{te}_{EL_1})\) and \(ic' \in \text{Dom}(\text{te}_{EL_1})\)

**Proof:** By definition of \(T_{EL_1}'(\text{act}_1)\) and \(\text{te}_{EL_1}(ic)\) (Def. B.3), assumption 3.1, assumption 5.1, and definition of \text{te}_{EL_1} (Eq. (15)).

**Proof:** By definition of \(T_{EL_1}'\) (Def. B.3), assumption 3.1, assumption 5.1, and definition of \text{te}_{EL_1} (Eq. (15)).

**Proof:** By definition of \(\cdot\) (Def. A.3).

\(\{9\} 3. \text{Q.E.D.}\)

**Proof:** By \(\{9\} 1\), \(\{9\} 2\), and assumption 2.3.

\(\{8\} 2. \text{Choose variable renaming functions } vr_1, vr_2 \in \text{Var} \rightarrow \text{Var such that } vr_1(EL) = EL_2 \text{ and } vr_2(EL) = EL_2 \text{ when}\)
\( v_{r_1} \) and \( v_{r_2} \) are lifted to event libraries.

**Proof:** By assumption 1.

(8.3) Choose \( a_{s_2} \in \{e, bx^r, e, e\} \) such that \( \text{preState}(a_{s_2}, \text{var}(EL)) = \text{preState}(a_{s_1}, \text{var}(EL)) \) and \( \text{preState}(a_{s_2}, (\text{var}(EL))(x)) = \text{preState}(a_{s_1}, (\text{var}(EL))(x)) \) for all \( x \in \text{var}(EL) \)

**Proof:** By assumptions 1, 2.9, and 4.1, and (8.1), (8.2), definition of \( \downarrow \) (Def. A.3), \( \text{preState}(\ldots, \ldots) \), and \( \text{preState}(\ldots, \ldots) \) (Def. D.2).

(8.4) \( a_{s}\in A \)

**Proof:** By (8.3) and assumption 2.8, and definition of \( A \) (Eq. (10)).

(8.5) \( \text{io}(a_{s_1,1}) = \text{io}(a_{s_2,1}) = \emptyset \)

**Proof:** By (8.1), (8.3) and definition of \( \downarrow \) (Def. A.3) and \( \text{to} \) (Eq. (11)).

(8.6) Choose \( a_{s_2,1} \in \{te_{EL_2}(ie)\} \) such that \( \text{preState}(a_{s_2,1}) = \text{postState}(a_{s_2,1}) \)

**Proof:** By (8.3), assumption 5.1, definition of \( \downarrow \) (Def. A.3), and definition of \( \text{preState}(\ldots, \ldots) \) and \( \text{postState}(\ldots, \ldots) \) (Def. D.2).

(8.7) \( a_{s_2,2} \in A \)

**Proof:** By assumption 2.4, assumption 2.6, (8.1), (8.3), (8.6), and definition of \( A \) (Eq. (10)).

(8.8) \( \text{io}(a_{s_2,2}) = \text{io}(a_{s_2,2}) \)

**Proof:** By assumption 2.6, assumption 2.8, (8.1), (8.3), (8.6), and definition of \( \text{io} \) (Eq. (11)).

(8.9) Choose \( a_{s_2,3} \in \{e, e, e, a'\} \) such that \( \text{preState}(a_{s_2,3}) = \text{postState}(a_{s_2,3}) \)

**Proof:** By definition of \( \downarrow \) (Def. A.3), and definition of \( \text{preState}(\ldots) \) and \( \text{postState}(\ldots) \) (Def. D.2).

(8.10) \( a_{s_2,4} \in A \)

**Proof:** By (8.9) and definition of \( A \) (Eq. (10)).

(8.11) \( \text{io}(a_{s_2,4}) = \text{io}(a_{s_2,4}) \)

**Proof:** By (8.1), (8.9), definition of \( \downarrow \) (Def. A.3), and definition of \( \text{io} \) (Eq. (11)).

(8.12) Choose \( a_{s_2} \in A \) such that \( a_{s_2} = a_{s_2,1} a_{s_2,2} a_{s_2,3} \)

**Proof:** By (8.4), (8.6), (8.7), (8.9), (8.10), and definition of \( A \) (Eq. (10)).

(8.13) \( \text{io}(a_{s_2}) = \text{io}(a_{s_2}) \)

**Proof:** By (8.1), (8.5), (8.6), (8.7), (8.8), (8.10), (8.12), and definition of \( \text{io} \) (Eq. (11)).

(8.14) Q.E.D.

**Proof:** By (8.3), (8.6), (8.9), (8.12) and (8.13).

(7.2) Q.E.D.

**Proof:** By (7.1) and definition \( \downarrow \) (Def. A.3).

(6.2) Q.E.D.

**Proof:** By assumption 5.1, (6.1), and definition of \( T_{EL_2}^r \) (Def. B.3).

(5.2) CASE: \( 5.1 \; ie \notin \text{Dom}(te_{EL_2}) \) and \( io' \notin \text{Dom}(te_{EL_2}) \)

(6.1) Choose \( a_{s_2} \in \{T_{EL_2}(act_1)\} \) such that \( a_{s_2} = a_{s_2} \)

**Proof:** By assumption 4.1., assumption 5.1, and definition of \( T_{EL_2}^r \) (Def. B.3), definition \( \downarrow \) (Def. A.3), and definition of
\[ tact_{EL}(\text{Eq. }\langle 15\rangle). \]

(7.2) Q.E.D.

**Proof:** By (7.1) and assumption 2.10.

(6.2) \( a_{s_1} \in A \)

**Proof:** By (6.1) and assumption 2.8.

(6.3) \( a_{s_1} = a_{s'_1} \)

(7.1) \( \{T_{EL_1}(\text{act}_1)\} = \{tact_{EL_1}(\text{act}_1)\} \)

**Proof:** By assumption 3.1, assumption 5.1, and definition of \( T_{EL_1} \) (Def. B.3), definition \( \{\_\} \) (Def. A.3), and definition of \( tact_{EL_1} \) (Eq. (15)).

(7.2) Q.E.D.

**Proof:** By (7.1), assumptions 2.3 and 2.5.

(6.4) \( io(a_{s_2}) = io(a_{s'_1}) \)

**Proof:** By (6.1), (6.2), and assumption 2.8.

(6.5) Q.E.D.

**Proof:** By (6.1), (6.2), and (6.4).

(5.3) Q.E.D.

**Proof:** By (5.1) and (5.2).

(4.3) **Case:** \( 3.1 \) \( \text{act}_2 = (\epsilon, b_{x'}, o_{x'}, a_{s_2}') \) for \( o_{x'} \in \text{OE} \cup \{\epsilon\}, b_{x'} \in \text{BExp} \cup \{\epsilon\}, \) and \( a_{s_2}' \in \text{Assign} \cup \{\epsilon\} \)

**Proof:** Case assumption 4.1 contradicts assumption 2.8 since assumption 4.1 implies that \( io(a_{s_2}) \neq io(a_{s_1}) \) by definition of \( \{\_\} \) (Def. A.3) and definition of \( io \) (Eq. (11)). Hence, assumption 4.1 cannot hold.

(4.4) Q.E.D.

**Proof:** By (4.1), (4.2), (4.3) and definition of \( \text{Act} \) (Eq. 7) and (Eq. 8).

(3.3) **Case:** \( 3.1 \) \( \text{act}_1 = (\epsilon, b_{x'}, o_{x'}, a_{s_1}) \) for \( o_{x'} \in \text{OE} \cup \{\epsilon\}, b_{x'} \in \text{BExp} \cup \{\epsilon\}, \) and \( a_{s_1} \in \text{Assign} \cup \{\epsilon\} \)

**Proof:** Proof is the same as for (3.2).

(3.4) Q.E.D.

**Proof:** By (3.1), (3.2), (3.3) and definition of \( \text{Act} \) (Eq. 7) and (Eq. 8).

(2.2) Q.E.D.

**Proof:** By (2.1) and logical implication (\( \rightarrow \)).

(1.2) **Case:** \( 1. \) \( n = n' + 1 \) for \( n' \in N \)

1.2 Vi \( \in N : i \geq 2 \land i \leq n' \implies \text{Ind}(P, Q, EL_1, EL_2, i) \)

(2.1) **Assume:** \( 2.1 j + k = n \) for \( j, k \in N \setminus \{0\} \)

2.2 \( (\text{act}_{1,1}, \ldots, \text{act}_{1,j}) \in pr(|P|) \)

2.3 \( a_{s_1,1} \in \{T_{EL_1}(\text{act}_{1,1})\} \land \cdots \land a_{s_1,j} \in \{T_{EL_1}(\text{act}_{1,j})\} \)

2.4 \( a_{s_1,1} \in A \) and \( a_{s_1,1} = a_{s_1,1} \land \cdots \land a_{s_1,j} \)

2.5 \( a_{s_1,1} \in \{\text{act}_{1,1}\} \land \cdots \land a_{s_1,j} \in \{\text{act}_{1,j}\} \)

2.6 Vi \( \in \{1, \ldots, j\} : \text{preState}(a_{s_1,1}, var(EL_1)) = \text{preState}(a_{s_1,j}, var(EL_1)) \land \)

(\( \text{postState}(a_{s_1,1}, var(EL_1)) = \text{postState}(a_{s_1,j}, var(EL_1)) \))

2.7 \( a_{s_2} \in pr(|Q|) \land A \) and \( a_{s_1,1} = a_{s_1,1} \land \cdots \land a_{s_1,j} \)

2.8 \( a_{s_2} \in pr(|Q|) \land A \) such that \( io(a_{s_2}) = io(a_{s_1}) \)

2.9 \( (\text{act}_{2,1}, \ldots, \text{act}_{2,k}) \in |Q| \)

2.10 \( a_{s_2,1} \in \{\text{act}_{2,1}\} \land \cdots \land a_{s_2,k} \in \{\text{act}_{2,k}\} \)

2.11 \( a_{s_2} = a_{s_1,1} \land \cdots \land a_{s_2,k} \)

**Proof:** \( 3 a_{s_2} \in \{T_{EL_1}(\text{act}_{1,1})\} \land \cdots \land \{T_{EL_1}(\text{act}_{1,k})\} \land a_{s_2} \in A \land \)

io(a_{s_2}) = io(a_{s_1})

(3.1) **Case:** \( 3.1 \) \( \text{act}_{1,1} = (\epsilon, b_{x'}, \epsilon, a_{s_1}) \) for \( b_{x'} \in \text{BExp} \cup \{\epsilon\} \) and \( a_{s_1} \in \text{Assign} \cup \{\epsilon\} \)
(4.1) Choose \( e \in \mathcal{A} \) such that \( e^1 = e \).  
**Proof:** By assumption 2.4 and definition of \( \mathcal{A} \) (Eq. (10)).

(4.2) Choose \( e_1 \in \mathcal{P} \cap \mathcal{A} \) such that \( e = e_1 \).  
**Proof:** By assumption 2.7, definition of \( \mathcal{A} \) (Eq. (10)), and definition of \( \mathcal{P} \) (Def. D.3).

(4.3) \( \mathcal{I}(e_1) = \mathcal{I}(e) \)  
**Proof:** By (4.2), assumptions 2.5, 2.8 and 3.1, and definition of \( \mathcal{I}(\cdot) \) (Def. A.3) and definition of \( \mathcal{I}(\cdot) \) (Eq. (11)).

(4.4) Q.E.D.

**Proof:** By assumptions 1.1, 1.2, 2.1, 2.2, 2.3, 2.5, 2.6, and 2.8 - 2.11, and (4.1), (4.2), and (4.3) since this implies that the antecedent of \( \text{Ind}(P, Q, \mathcal{E}_1, \mathcal{E}_2, \mathcal{N}) \) is satisfied for \( j = 1 \) and \( k \).

(3.2) CASE: \( 3 \)  
**CASE:** \( 4.1 \) \( \mathcal{A}_1 \in \mathcal{A} \) such that \( e \in \mathcal{I} \), \( b_1 \in \mathcal{B}_\mathcal{E}_1 \cup \{e\} \), and \( e \in \mathcal{I} \) (Eq. (10)), and definition of \( \mathcal{I}(\cdot) \) (Def. D.3).

(5.1) Choose \( e_2 \in \mathcal{P} \cap \mathcal{A} \) such that \( e_2 = e_1 \).  
**Proof:** By assumptions 2.8 and 2.11, and definition of \( \mathcal{A} \) (Eq. (10)), and definition of \( \mathcal{P} \) (Def. D.3).

(5.2) \( \mathcal{I}(e_1) = \mathcal{I}(e) \)  
**Proof:** By (5.1), assumptions 4.1 and 2.8, and definition of \( \mathcal{I}(\cdot) \) (Def. A.3) and definition of \( \mathcal{I}(\cdot) \) (Eq. (11)).

(5.3) Choose \( e_3 \in \{T_{\mathcal{E}_1}(\mathcal{A}_2, 1)\} \cdots \{T_{\mathcal{E}_1}(\mathcal{A}_2, k - 1)\} : e_3 \in \mathcal{A} \land \mathcal{I}(e_3) = \mathcal{I}(e) \)  
**Proof:** By assumptions 1.1, 1.2, 2.1 - 2.7, 2.9 - 2.11, and (5.1) and (5.2) since this implies that the antecedent of \( \text{Ind}(P, Q, \mathcal{E}_1, \mathcal{E}_2, \mathcal{N}) \) is satisfied for \( j = 1 \) and \( k = 1 \).

(5.4) Choose \( e_2 \in \{T_{\mathcal{E}_1}(\mathcal{A}_2, 1)\} \cdots \{T_{\mathcal{E}_1}(\mathcal{A}_2, k - 1)\} \cap \mathcal{A} \) such that \( \mathcal{I}(e_2) = \mathcal{I}(e) \) and \( \text{preState}(e_2) = \text{postState}(e_2) \)  
**Proof:** By (5.3), assumption 4.1, assumption 1, assumption 2.8 - 2.11, definition of \( T_{\mathcal{E}_1}(\mathcal{A}_2, k - 1) \) (Def. B.3), and definition of \( \mathcal{I}(\cdot) \) (Def. A.3).

(5.5) \( e_3 \in \mathcal{A} \) and \( \mathcal{I}(e_3) = \mathcal{I}(e) \)  
**Proof:** By (5.3), (5.4), definition of \( \mathcal{A} \) (Eq. (10)), and definition of \( \mathcal{I}(\cdot) \) (Eq. (11)).

(5.6) Q.E.D.

**Proof:** By (5.3), (5.4), and (5.5).

(4.2) CASE: \( 4.1 \) \( \mathcal{A}_2 \in \mathcal{A} \) such that \( e \in \mathcal{I} \), \( b_2 \in \mathcal{B}_\mathcal{E}_1 \cup \{e\} \), and \( e \in \mathcal{I} \) (Eq. (10)), and definition of \( \mathcal{I}(\cdot) \) (Def. A.3).

(5.1) Choose \( e_4 \in \mathcal{P} \cap \mathcal{A} \) such that \( e_4 = e_1 \).  
**Proof:** By assumption 2.4 and definition of \( \mathcal{A} \) (Eq. (10)).

(5.2) Choose \( e_5 \in \mathcal{P} \cap \mathcal{A} \) such that \( e_5 = e_4 \).  
**Proof:** By assumption 2.7, definition of \( \mathcal{A} \) (Eq. (10)), and definition of \( \mathcal{P} \) (Def. D.3).

(5.3) Choose \( e_6 \in \mathcal{P} \cap \mathcal{A} \) such that \( e_6 = e_5 \).  
**Proof:** By assumptions 2.8 and 2.11, and definition of \( \mathcal{A} \) (Eq. (10)), and definition of \( \mathcal{P} \) (Def. D.3).

(5.4) \( \mathcal{I}(e_1) = \mathcal{I}(e) \)  
**Proof:** By (5.2), (5.3), assumptions 2.8, 3.1, and 4.1, and definition of \( \mathcal{I}(\cdot) \) (Eq. (11)).

(5.5) Choose \( e_7 \in \{T_{\mathcal{E}_1}(\mathcal{A}_2, 1)\} \cdots \{T_{\mathcal{E}_1}(\mathcal{A}_2, k - 1)\} \cap \mathcal{A} \) such that
that $io(aas_1') = io(aas_2')$

**Proof:** By assumptions 1.1, 1.2, 2.1, 2.2, 2.3, 2.4, 2.5, 2.6, 2.7, 2.10, 2.11, and (5)1 - (5)4 since this implies that the antecedent of $Ind(P, Q, EL_1, EL_2, n' - 1)$ is satisfied for $j - 1$ and $k - 1$.

(6) Choose $aas_2' \in \{T^{EL_2}(act_{EL_2})\} \cap A$ such that $io(aas_2') = io(aas_1')$ and $postState(aas_2') = postState(aas_1')$

(6.1) **CASE:** $5.1$ $ie \in Dom(\text{te}_{EL_1})$ and $i' e \in \text{Dom}(\text{te}_{EL_2})$

(7.1) $\exists aas_2' \in \{\langle c, bx, e, c e \rangle, \text{te}_{EL_2}(ie')(e, e, c, e)\} \cap A \wedge io(aas_2') = io(aas_1') \wedge postState(aas_2') = postState(aas_1')$

(8.1) $\exists aas_2' \in \{\langle c, bx', e, c \rangle \} \wedge \{\langle \text{te}_{EL_2}(ie') \rangle \} \wedge \{\langle c, e, c, a \rangle \} \cap A \wedge$

$io(aas_2') = io(aas_1') \wedge postState(aas_2') = postState(aas_1')$

(9.1) Choose $aas_1' \in \{\langle c, bx, e, c \rangle \}$, $aas_2' \in \{\text{te}_{EL_1}(ie)\}$, and $aas_3' \in \{\langle c, e, c, e \rangle \}$ such that $aas_1' \sim aas_3' \sim aas_2' \sim aas_3'$

(10.1) $\{\text{te}_{EL_1}(act_{EL_1})\} = \{\text{te}_{EL_2}(act_{EL_2})\} = \{\langle c, bx, e, c \rangle, \text{te}_{EL_1}(ie).\{\langle c, e, c, a \rangle \}\}$

**Proof:** By definition of $T_{EL_1}$ (Def. B.3), assumption 3.1, and definition of $\text{te}_{EL_1}$ (Eq. (15)).

(10.2) $\{\langle c, bx', e, c \rangle, \text{te}_{EL_2}(ie).\{\langle c, e, c, a \rangle \}\} = \{\langle c, bx, e, c \rangle \} \sim \{\text{te}_{EL_2}(ie)\} \sim \{\langle c, e, c, a \rangle \}$

**Proof:** By definition of (\_\_\_) (Def. A.3).

(10.3) Q.E.D.

(9.2) Choose variable renaming functions $\vartheta_1, \vartheta_2 \in \text{Var} \rightarrow \text{Var}$ such that $\vartheta_1(EL) = EL_2$ and $\vartheta_2(EL) = EL_1$

when $\vartheta_1$ and $\vartheta_2$ are lifted to event libraries.

**Proof:** By assumption 1.

(9.3) Choose $aas_2' \in \{\langle c, bx', e, c \rangle \}$ such that $postState(aas_2') = postState(aas_1')$ and $\text{preState}(aas_2') = \{\text{preState}(aas_1')(\vartheta_1(x))\}$

for all $x \in \text{var}(EL)$

**Proof:** By assumptions 1, 2.9, and 4.1, and (5)5, (9)1, definition of (\_\_\_) (Def. A.3), and definition of $\text{preState}()$ (Def. D.2).

(9.4) $aas_2' \in A$

**Proof:** By (9)3, assumption 2.8, assumption 2.10, assumption 4.1, and definition of $A$ (Eq. (10)).

(9.5) $io(aas_1')(A) = io(aas_2')$

**Proof:** By (9)1, (9)3 and definition of (\_\_\_) (Def. A.3) and $io$ (Eq. (11)).

(9.6) Choose $aas_2' \in \{\text{te}_{EL_2}(ie')\}$ such that $\text{preState}(aas_2') = \text{postState}(aas_1')$

**Proof:** By (9)3, assumption 5.1, definition of (\_\_\_) (Def. A.3), and definition of $\text{preState}()$ and $\text{postState}()$ (Def. D.2).

(9.7) $aas_2' \in A$

**Proof:** By assumption 2.4, assumption 2.6, (9)1, (9)3, (9)6, and definition of $A$ (Eq. (10)).

(9.8) $io(aas_2') = io(aas_2')$

**Proof:** By assumption 2.6, assumption 2.8, (9)1, (9)3, (9)6, and definition of $io$ (Eq. (11)).

(9.9) Choose $aas_2' \in \{\langle c, e, c, a \rangle \}$ such that $\text{preState}(aas_2') = \text{postState}(aas_2')$
PROOF: By definition of \( \downarrow \) (Def. A.3), and definition of \( \text{preState}(\cdot) \) and \( \text{postState}(\cdot) \) (Def. D.2).

(9.10) \( aas'_{2,3} \in A \)
PROOF: By (9.9) and definition of \( A \) (Eq. (10)).

(9.11) \( i\downarrow (aas'_{2,3}) = i\downarrow (aas'_{1,3}) \)
PROOF: By (9.1), (9.9), definition of \( \downarrow \) (Def. A.3), and definition of \( i\downarrow \) (Eq. (11)).

(9.12) Choose \( aas'_{2} \in A \) such that \( aas'_{2} = aas'_{2,1} - aas'_{2,2} - aas'_{2,3} \)
PROOF: By (9.4), (9.6), (9.7), (9.9), (9.10), and definition of \( A \) (Eq. (10)).

(9.13) \( i\downarrow (aas'_{i,j}) = i\downarrow (aas'_{2}) \)
PROOF: By (9.1), (9.5), (9.8), (9.11), (9.13), and definition of \( i\downarrow \) (Eq. (11)).

(9.14) Q.E.D.
PROOF: By (9.12), (9.13).

(8.2) Q.E.D.
PROOF: By (8.1) and definition \( \{\}_\downarrow \) (Def. A.3).

(7.2) Q.E.D.
PROOF: By assumption 5.1, (7.1), and definition of \( T'_{\text{EL}} \) (Def. B.3).

(6.2) CASE: \( i \notin \text{Dom}(t\text{eEL}) \) and \( i' \notin \text{Dom}(t\text{eEL}) \)

(7.1) Choose \( aas_{2,k} \in \{T'_{\text{EL}}(\text{act}_{2,k})\} \) such that \( aas_{2,k} = aas_{2,k} \)

(8.1) \( \{T'_{\text{EL}}(\text{act}_{2,k})\} = \{t\text{act}_{\text{EL}}(\text{act}_{2,k})\} = \{aas_{2,k}\} \)
PROOF: By assumption 4.1, assumption 5.1, and definition of \( T'_{\text{EL}} \) (Def. B.3), and definition of \( t\text{act}_{\text{EL}} \) (Eq. (15)).

(8.2) Q.E.D.
PROOF: By (8.1) and assumption 2.10.

(7.2) \( aas_{2,k} \in A \)
PROOF: By (7.1) and assumption 2.8.

(7.3) \( aas_{i,j} = aas_{i,j} \)

(8.1) \( \{T'_{\text{EL}}(\text{act}_{1,j})\} = \{t\text{act}_{\text{EL}}(\text{act}_{1,j})\} = \{aas_{1,j}\} \)
PROOF: By assumption 3.1, assumption 5.1, and definition of \( T'_{\text{EL}} \) (Def. B.3), and definition of \( t\text{act}_{\text{EL}} \) (Eq. (15)).

(8.2) Q.E.D.
PROOF: By (8.1), assumptions 2.3, 2.5, and 2.6.

(7.4) \( i\downarrow (aas_{2,k}) = i\downarrow (aas_{1,j}) \)
PROOF: By (7.1), (7.3), and assumption 2.8.

(7.5) Q.E.D.
PROOF: By (7.1), (7.2), and (7.4).

(6.3) CASE: \( i \notin \text{Dom}(t\text{eEL}) \) and \( i' \in \text{Dom}(t\text{eEL}) \)
PROOF: Case assumption 4.1 contradicts assumption 2.8 since assumption 4.1 implies that \( i\downarrow (aas_{2}) \neq i\downarrow (aas_{1}) \) by assumption 1, definition of \( \text{rnmm} \) (App.B.1), definition of \( i\downarrow \) (Eq. (11)) and definition of \( t\text{eEL} \) (Eq. 14). Hence, the case assumption cannot hold.

(6.4) CASE: \( i \in \text{Dom}(t\text{eEL}) \) and \( i' \notin \text{Dom}(t\text{eEL}) \)
PROOF: Case assumption 4.1 contradicts assumption 2.8 since assumption 4.1 implies that \( i\downarrow (aas_{2}) \neq i\downarrow (aas_{1}) \) by assumption 1, definition of \( \text{rnmm} \) (App.B.1), definition of \( i\downarrow \) (Eq. (11)) and definition of \( t\text{eEL} \) (Eq. 14). Hence, the case assumption cannot hold.

(6.5) Q.E.D.
PROOF: By (6)1 and (6)2, (6)3, and (6)4.

(5)7. Q.E.D.

PROOF: By (5)5 and (5)6.

(4)3. CASE: 4.1 \(ab \in K = (\epsilon, bx, c, c')\) for \(ce \in OE, bx' \in BExp \cup \{\epsilon\}\), and \(a' \in Assign \cup \{\epsilon\}\)

PROOF: Case assumption 4.1 contradicts assumption 2.8 since assumption 4.1 implies that \(iso(a_2) \neq iso(a_1)\) by definition of \(\neg\left(\right)\) (Def. A.3) and definition of \(iso\) (Eq. (11)). Hence, assumption 4.1 cannot hold.

(3)4. Q.E.D.

PROOF: By (4)1, (4)2, (4)3 and definition of \(Act\) (Eq. (7)) and (Eq. (8)).

(3)3. CASE: 3.1 \( \alpha = (\epsilon, bx, c, a_3)\) for \(ce \in OE, bx \in BExp \cup \{\epsilon\}\), and \(a_3 \in Assign \cup \{\epsilon\}\)

PROOF: Proof is the same as for (3)2.

(3)2. Q.E.D.

PROOF: By (3)1, (3)2, (3)3 and definition of \(Act\) (Eq. (7)) and (Eq. (8)).

(2)2. Q.E.D.

PROOF: By (2)1 and logical implication \((\implies)\).

(1)3. Q.E.D.

PROOF: By (1)2 and (1)2.

Lemma B.1 If \(EL\) is an event library, and \(P_1\) and \(P_2\) be basic state machines whose variables are disjoint from those in \(EL\), then \([T'_{EL}]\) is homomorphic w.r.t. union, i.e.,

\[
[T'_{EL}([P_1] \cup [P_2])] = [T'_{EL}([P_1]) \cup [T'_{EL}([P_2])]
\]

Proof of Lemma B.1.1
\[
[T'_{EL}(P_1) + T'_{EL}(P_2)] = [T'_{EL}(P_1) + T'_{EL}(P_2)] \quad \text{Def. B.3}
\]
\[
[T'_{EL}(P_1 + P_2)] = [T'_{EL}(P_1) + T'_{EL}(P_2)] \quad \text{Def. B.4}
\]
\[
[T'_{EL}([P_1] \cup [P_2]) = [T'_{EL}([P_1]) \cup [T'_{EL}([P_2])]) \quad \text{Lemma B.1.2}
\]

Lemma B.1.2 \([P + Q] = [P] \cup [Q]\)

Proof of Lemma B.1.2
\[
[P + Q] = [P + Q] \quad \text{Def. A.4}
\]
\[
[P + Q] = iso([P + Q] \cap A) \quad \text{Def. A.4}
\]
\[
[P + Q] = iso([P] \cup [Q] \cap A) \quad \text{Def. A.3}
\]
\[
[P + Q] = iso([P] \cap A) \cup iso([Q] \cap A) \quad \text{By (1)1}
\]
\[
[P + Q] = [P] \cup [Q] \quad \text{Def. A.4}
\]

Lemma B.2 If \(EL\) is an event library, and \(P\) and \(Q\) be basic state machines whose variables are disjoint from those in \(EL\), then \([T'_{EL}]()\) is homomorphic w.r.t. concatenation, i.e.,

\[
[T'_{EL}([P] \cdot [Q])] = [T'_{EL}([P]) \cdot [Q]]
\]
Proof of Lemma B.2
ASSUME: 1. $\text{var}(E_L) \cap (\text{var}(Q) \cup \text{var}(P)) = \emptyset$
PROVE: $[T'_{EL}([P]) \cap [T'_{EL}([Q])] = [T'_{EL}([P]) \cap [Q]]$

(1.1) Choose $Q' \in Q$ such that $[Q] = [Q']$, and $[P, Q'] = [P] \cap [Q']$
PROOF: Assume that all variables in $Q$ are assigned to a value before they are used in a guard. Then execution of $Q$ will be the same even if we choose an arbitrary initial data state. Therefore we must have that $[P, Q] = [P] \cap [Q]$. However, if $Q$ has unassigned variables in guards, then executing $P$ before executing $Q$ might affect the execution of $Q$. Therefore it may be the case that $[P, Q] \neq [P] \cap [Q]$. However, we can always rename the variables of $Q$ to obtain the state machine $Q'$ such that $[Q] = [Q']$ and $\text{var}(P) \cap \text{var}(Q') \neq \emptyset$. In this case, executing $P$ before $Q'$ will not affect the execution of $Q'$. Therefore $[P, Q'] = [P] \cap [Q']$.

(1.2) $[T'_{EL}([P]) \cap [T'_{EL}([Q])] = [T'_{EL}([P]) \cap [Q]]$
PROOF:

$[T'_{EL}([P]) \cap [T'_{EL}([Q])] = [T'_{EL}([P]) \cap [T'_{EL}([Q])]$  Def. B.3

$[T'_{EL}([P]) \cap [T'_{EL}([Q])] = [T'_{EL}([P]) \cap [T'_{EL}([Q])]$  (1.1)

$[T'_{EL}([P]) \cap [T'_{EL}([Q])] = [T'_{EL}([P]) \cap [T'_{EL}([Q])]$  Def. B.4

$[T'_{EL}([P]) \cap [T'_{EL}([Q])] = [T'_{EL}([P]) \cap [T'_{EL}([Q])]$  (1.1) and ass. 1

(1.3) Q.E.D.

PROOF: By (1.2) and Theorem B.1

D.3 Composite transformations

Theorem B.2 The relation $[T'_{EL}^G]$ is a function when restricted to the image of $T'_{EL}$, i.e.,

$[P] \in [Q] \Rightarrow [T'_{EL}^G([P])] \cap \text{Im}_{EL} = [T'_{EL}^G([Q])] \cap \text{Im}_{EL}$

Proof of Theorem B.2

ASSUME: 1. $[P_1 \parallel \cdots \parallel P_n] = [Q_1 \parallel \cdots \parallel Q_n]$

PROVE: $[T'_{EL}^G([P_1 \parallel \cdots \parallel P_n])] \cap \text{Im}_{EL} = [T'_{EL}^G([Q_1 \parallel \cdots \parallel Q_n])] \cap \text{Im}_{EL}$

(1.1) ASSUME: 1.1 $s' \in [T'_{EL}^G([P_1 \parallel \cdots \parallel P_n])] \cap \text{Im}_{EL}$

PROVE: $s' \in [T'_{EL}^G([Q_1 \parallel \cdots \parallel Q_n])] \cap \text{Im}_{EL}$

(2.1) Choose $E_L \in E_L$ such that $E_L = \text{rnm}([P_1 \parallel \cdots \parallel P_n], EL)$

PROOF: By definition of rnm (see App. B.1)

(2.2) Choose $s'_1 \in [T'_{EL}([P_1])], \ldots, s'_n \in [T'_{EL}([P_n])]$ such that $s' \in [(s'_1, \ldots, s'_n) \cap \text{Im}_{EL}]$

PROOF: By assumption 1.1 and definition of $[\_]$ (Def. A.5), and definition of $T'_{EL}^G$ (Def. B.5)

(2.3) Choose $s_1 \in [P_1], \ldots, s_n \in [P_n]$ such that $s'_1 \in [T'_{EL}([s_1])], \ldots, s'_n \in [T'_{EL}([s_n])$

PROOF: By (2.1), (2.2), Theorem B.1 (which ensures that $[T'_{EL}^G([\_])$ is a function) and Lemma B.1 (which ensures that $[T'_{EL}^G([\_])$ is defined for singleton sets).

(2.4) Choose $s \in [P_1 \parallel \cdots \parallel P_n]$ such that $s \in (s_1, \ldots, s_n)$

PROOF: By (2.2), (2.3), definition of $[\_]$ (Def. A.5) and definition of $\text{Im}_{EL}$ (Eq. (16)).
D PROOFS

(2.5) \( s \in \parallel Q_1 \parallel \cdots \parallel Q_n \parallel \)

PROOF: By (2.4) and assumption 1.

(2.6) \( t_i \in \parallel Q_1 \parallel \cdots \parallel Q_n \parallel \) such that \( s \in \parallel t_1, \ldots, t_n \parallel \)

PROOF: By (2.3), (2.4), (2.5), definition of \( \parallel \parallel \) (Def. A.5), and definition of composite state machine expressions Def. A.2.

(2.7) \( t_i = s_i \) for all \( i \in \{1, \ldots, n\} \)

PROOF: By definition of a composite state machine, each basic state machine consists of must have different names. Therefore \( \parallel Q_i \parallel \cap \parallel Q_j \parallel \neq \emptyset \) for all \( i, j \in \{1, \ldots, n\} \) such that \( i \neq j \). By definition of parallel composition, there is one and only one trace \( t_i \) in each basic state machine \( Q_i \) (for \( i \in \{1, \ldots, n\} \)) such that \( s \in \parallel t_1, \ldots, t_n \parallel \). The same holds for the basic state machines \( P_i \), therefore we have that \( s_i \) must be equal to \( t_i \).

(2.8) Choose \( EL_2 \in EL \) such that \( EL_2 = \text{runm}(Q_1 \parallel \cdots \parallel Q_n, EL) \)

PROOF: By definition of \( \text{runm} \) (see App. B.1).

(2.9) \( s' \in \parallel (T'_{EL}(\{s_1\}), \ldots, T'_{EL}(\{s_n\})) \cap \text{Im}_E \)

PROOF: By (2.2), (2.3), (2.8), Theorem B.1 (which ensures that \( T_{EL}(\_\_\_) \) is a function).

(2.10) Q.E.D.

PROOF: By (2.2), (2.3), (2.6), (2.7), (2.9), and definition of \( \parallel \parallel \) (Def. A.5).

(1.2) Q.E.D.

PROOF: By (1.1) and symmetry of =.

Lemma B.3 The semantics of the event transformation for composite state machines induced by an event library \( EL \) is homomorphic w.r.t. the union operator when restricted to its image, i.e.,

\[ T_{EL}(\parallel P \parallel \cup \parallel Q \parallel) = T_{EL}(\parallel P \parallel) \cup T_{EL}(\parallel Q \parallel) \]

when \( \text{var}(EL) \cap \text{var}(P) \cap \text{var}(Q) = \emptyset \)

Proof of Lemma B.3 Assume \( \text{var}(EL) \cap \text{var}(P) \cap \text{var}(Q) = \emptyset \), then we have

\[ \bigcup_{e \in \{P \cup \{Q\}} T_{EL}(e)(s) = \bigcup_{e \in \{P \} \cup \{Q\}} T_{EL}(e)(s) \]

\[ T_{EL}(\parallel P \parallel \cup \parallel Q \parallel) = T_{EL}(\parallel P \parallel) \cup T_{EL}(\parallel Q \parallel) \] 

By Lemma B.3.1

Lemma B.3.1 Let the function \( T_{EL}^C \in E^* \rightarrow P(E^*) \) be defined by

\[ T_{EL}^C(s) \overset{\text{def}}{=} (T'_{EL}(\{s|e_{nm_1}\}), \ldots, T'_{EL}(\{s|e_{nm_n}\})) \]

for all \( s \in T \) and \( Nm = \{nm_1, \ldots, nm_n\} \).

Then the semantics of the transformation \( T_{EL}(P) \) is entirely characterized by \( T_{EL}^C \) if \( \text{var}(EL) \cap \text{var}(P) = \emptyset \), i.e.,

\[ T_{EL}(\parallel P \parallel) = \bigcup_{s \in \{P \}} T_{EL}^C(s) \]

Proof of Lemma B.3.1

PROVE: \( T_{EL}(\parallel P \parallel) = \bigcup_{s \in \{P \}} T_{EL}^C(s) \)

LET: \( P = P_1 \parallel \cdots \parallel P_n \)
D PROOFS

(1.1. Assume: \(1.1 \var(EL) \cap \var(P) = \emptyset\)

\[1.2 \ s' \in \bar{T}_{EL}(\{P\})\]

prove: \(s' \in \bar{T}_{EL}(s)\) for some \(s \in \{P\}\)

(2.1. Choose \(s_1 \in \{P_1\}, \ldots, s_n \in \{P_n\}\) such that \(s' \in \bar{T}_{EL}(\{(s_1)\}, \ldots, \bar{T}_{EL}(\{(s_n)\}) \cap \text{Im}_{EL}\)

proof: By assumption 1.1, assumption 1.2, definition of \(\bar{T}_{EL}\) (Def. A.5), and definition of \(\bar{T}_{EL}\) (Eq. (17)).

(2.2. Choose \(s \in \{P\}\) such that \(s \in \bar{T}_{EL}(\{(s_1), \ldots, s_n\})\)

proof: By (2.1), definition of \(P\), definition of \(\bar{T}_{EL}\) (Def. A.5) and definition of \(\text{Im}_{EL}\) (Eq. (16)).

(2.3. \(s_1 = s|_{e_{nm_1}}, \ldots, s_n = s|_{e_{nm_n}}\) for some \(nm_1, \ldots, nm_n \in Nm\)

proof: By (2.1), definition of \(P\) and definition of \(D^G\) (Def. A.2).

(2.4. Q.E.D.

proof: By (2.1), (2.2), (2.3) and definition of \(\bar{T}_{EL}\)

(1.2. Assume: \(1.1 \var(EL) \cap \var(P) = \emptyset\)

\[1.2 \ s' \in \bar{T}_{EL}(\{P\})\]

prove: \(s' \in \bar{T}_{EL}(s)\) for arbitrary \(s \in \{P\}\)

(2.1. Choose \(s_1 \in \{P_1\}, \ldots, s_n \in \{P_n\}\) and \(nm_1, \ldots, nm_n \in Nm\) such that \(s_1 = s|_{e_{nm_1}}, \ldots, s_n = s|_{e_{nm_n}}\)

proof: By assumption 1.1, assumption 1.2, definition of \(\bar{T}_{EL}\) (Def. A.5), and definition of \(\text{Im}_{EL}\) (Eq. (9)).

(2.2. \(s' \in \bar{T}_{EL}(\{(s_1)\}, \ldots, \bar{T}_{EL}(\{(s_n)\}))\)

proof: By assumption 1.1, assumption 1.2, (2.1), and definition of \(\bar{T}_{EL}\).

(2.3. Q.E.D.

proof: By (2.1), (2.2), definition of \(\bar{T}_{EL}\) (Def. A.5), and definition of \(\bar{T}_{EL}(\{P\})\) (Def. B.5).

(1.3. Q.E.D.

proof: By (1.1) and (1.2).

D.4 Adherence preservation under event transformations

Theorem B.3 Let \(r\) be a restriction, and \(h\) be a high-level relation, and \(\bar{T}_{EL}\) be the transformation induced by event library \(EL\). Then \(\bar{T}_{EL}\) preserves \(\text{BSP}_{th}\) for specification \(P\) if the following conditions are satisfied for all \(s, t, u \in \{P\}, s' \in \bar{T}_{EL}(s), t' \in \bar{T}_{EL}(t)\)

\[s' \xrightarrow{r} t' \implies s \xrightarrow{r} t\]

\[s \xrightarrow{r} t \land s \xrightarrow{h} u \xrightarrow{\sim} t \implies \exists u' \in \bar{T}_{EL}(\{u\}) : s' \xrightarrow{h} u' \xrightarrow{\sim} t'\]

proof of theorem B.3

assume: 1. \(s, t \in \{P\}, s' \in \bar{T}_{EL}(s), t' \in \bar{T}_{EL}(t) : s' \xrightarrow{r} t' \implies s \xrightarrow{r} t\)

2. \(s, t, u \in \{P\}, s' \in \bar{T}_{EL}(s), t' \in \bar{T}_{EL}(t) : s \xrightarrow{r} t \land s \xrightarrow{h} u \xrightarrow{\sim} t \implies \exists u' \in \bar{T}_{EL}(u) : s' \xrightarrow{h} u' \xrightarrow{\sim} t'\)

prove: \(\text{BSP}_{th}(\{P\}) \implies \text{BSP}_{th}(\bar{T}_{EL}(\{P\}))\)

(1.1. assume: \(\text{BSP}_{th}(\{P\})\)

1.2 \(s' \xrightarrow{r} t'\) for \(s', t' \in \bar{T}_{EL}(\{P\})\)

prove: \(\exists u' \in \bar{T}_{EL}(\{P\}) : s' \xrightarrow{h} u' \xrightarrow{\sim} t'\)
(2.1) Choose \( s, t \in [P] \) such that \( s \mathrel{\downarrow} t, s' \in \overline{T}_{EL}(\{s\}) \), and \( t' \in \overline{T}_{EL}(\{t\}) \)

**Proof:** By assumption 1, assumption 1.2, and Lemma B.3.

(2.2) Choose \( u \in [P] \) such that \( s \mathrel{\downarrow} u \sim t \)

**Proof:** By assumption 1.1, (2)1 and definition of \( B_{SP_{\mathcal{E}}} \) (Eq. (18)).

(2.3) Choose \( u' \in \overline{T}_{EL}(\{u\}) \) such that \( s' \mathrel{\downarrow} u' \sim t' \)

**Proof:** By (2)1, (2)2, and assumption 2.

(2.4) \( u' \in \overline{T}_{EL}(\{P\}) \)

**Proof:** By (2)2, (2)3, and Lemma B.3.

(2.5) Q.E.D.

**Proof:** By (2)3 and (2)4.

(1.2) Q.E.D.

**Proof:** By (1)1 and definition of \( B_{SP_{\mathcal{E}}} \) (Eq. (18)).